

# LCFC Confidential

## CG413/CG513 MB Schematics Document

Intel Skylake-U22&U23/Kabylake-U22&U23 with DDR4

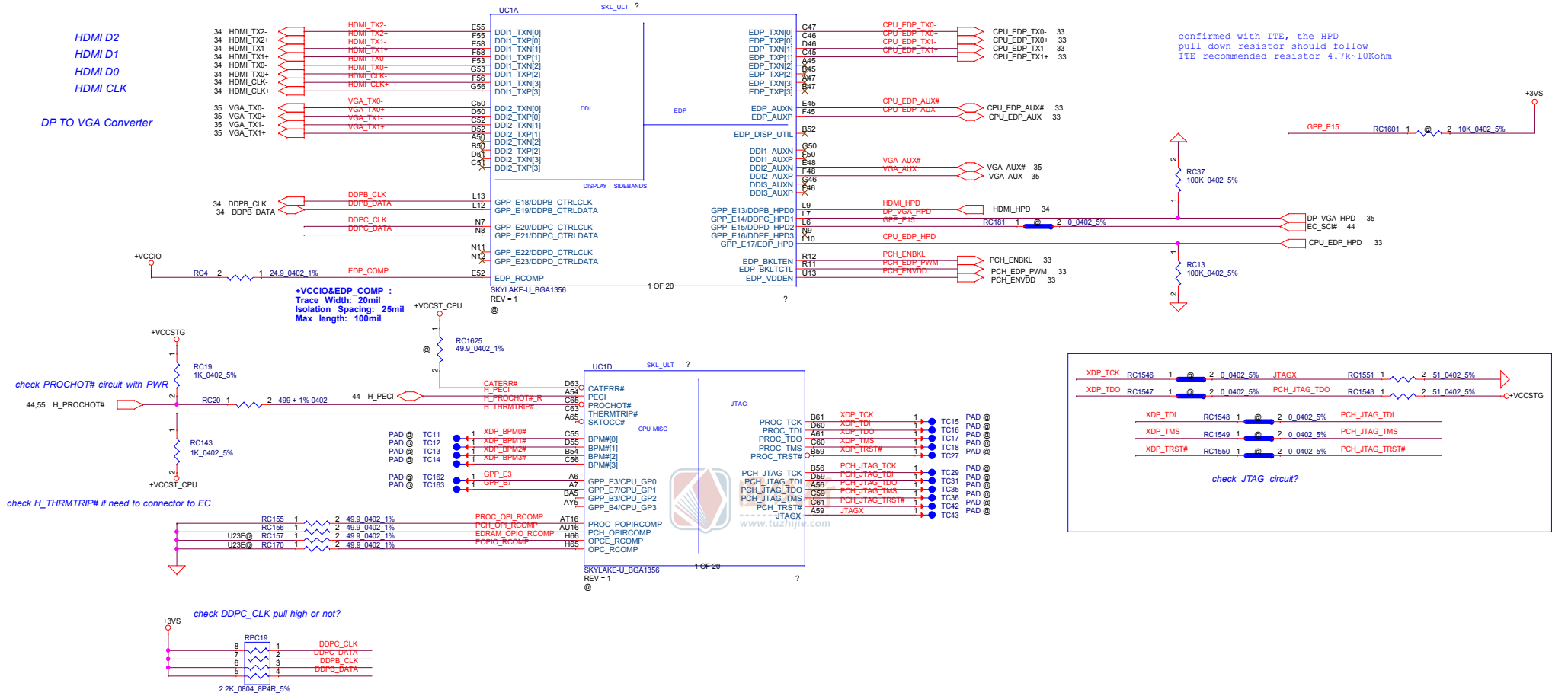
2016-06-12

REV: 1.0



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| Security Classification                                                                                                                                                                                                                                                                                                                                                                                                                 |            | LC Future Center Secret Data |            | Title                         |                                 |
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DDP\_CTRLDATA strapping sampled on the rising edge of PWROK

| Port   | Strap         | Enable                        | Disable |
|--------|---------------|-------------------------------|---------|
| Port 1 | DDPB_CTRLDATA | Pull up to 3.3 V with 2.2Kohm | NC      |
| Port 2 | DDPC_CTRLDATA | Pull up to 3.3 V with 2.2Kohm | NC      |

|                         |  |                              |  |                         |  |
|-------------------------|--|------------------------------|--|-------------------------|--|
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