

N61 CARRIER BUILD

N61 BOM CALLOUTS

NAND BOM OPTIONS

ALTERNATE NAND BOM OPTIONS

SHIELD BOM OPTIONS

ALTERNATE BOM OPTIONS

PAGE	页数	CONTENTS	目录		
2	2	SOC:MAIN	讲述CPU的HSIC,USB,TTAG通信总线 and 供电	N56_MLB	08/29/2013
3	3	SOC:I/OS	A8的激活使能控制,总线UART,I2C,I2C总线接口的描述	N56_MLB	08/29/2013
4	4	SOC:VDDCA,VDDI1/2,VDD,VDD_CPU,VDD_GPU	cpu供电	N56_MLB	08/29/2013
5	5	SOC:GND,VDDI018,VDDI0D,VDD_VAR_SOC	cpu vdd供电	N56_MLB	08/29/2013
6	6	SOC:NAND	硬盘	N56_MLB	08/29/2013
7	7	SOC:CAM,LCD,LDPD,PCIE	cpu与摄像头,液晶屏通信	N56_MLB	08/29/2013
8	8	IO:BUTTON_FLEX_CONN	开机接口,声音升降接口	N61_MLB	08/26/2013
9	9	AUDIO:L67_CODEC(1/2)	音频	N61_MLB	08/26/2013
10	10	AUDIO:L67_CODEC(2/2)	音频	N61_MLB	08/26/2013
11	11	CAMERA:FRONT_FLEX_CONN	前置摄像头,听筒,感光,麦克风	N62_MLB111	08/26/2013
12	12	POWER:ADI(1/2)	电源IC	N56_MLB	08/29/2013
13	13	POWER:ADI(2/2)	电源IC	N56_MLB	08/29/2013
14	14	POWER:TIGRISR,VIBE_DRIVER	充电IC和振动马达IC	N61_MLB	08/21/2013
15	15	DISPLAY:CHESTNUT,BACKLIGHT_DRIVER	显示触摸IC,指示灯IC	N61_MLB	08/26/2013
16	16	AUDIO:SPKR_AMP,STROBE	铃声放大IC,闪光灯IC	N61_MLB	08/26/2013
17	17	IO:TRISTAR2	USB控制器	N61_MLB	08/26/2013
18	18	IO:DOCK_FLEX_CONN	尾插接口	N61_MLB	08/26/2013
19	19	SENSORS:COMPASS	指南针	N61_MLB	08/26/2013
20	20	DISPLAY:FLEX_CONN	液晶接口	N61_MLB	08/26/2013
21	21	SENSORS:MESA_FLEX_CONN	指纹接口	N61_MLB	08/26/2013
22	22	SENSORS:OSCAR,CARBON,PHOS,MAGNESIUM	M8协处理器, M8协处理器芯片,加速计,陀螺仪,重力感应	N61_MLB	08/26/2013
23	23	CAMERA:REAR_FLEX_CONN	主摄像头接口	N61_MLB	08/26/2013
24	24	TOUCH:CUMULUS,MESON	触摸	N/A	N/A
25	25	POWER:BATT_CONN,TPS,PD_FEATURES	电池接口,电压测试	N61_MLB	08/26/2013
26	26	SYSTEM:VOLTAGE_PROPERTIES	电压综合统计	N56_MLB	09/10/2013
27	27	SYSTEM:N61_SPECIFIC	BOARD信号电压	N56_MLB	09/10/2013
28	28	BLANK	空白页	N56_MLB	09/10/2013
29	30	CELL:ALIASES	信号名称变更		
30	31	AP_INTERFACE & DEBUG_CONNECTORS	信号测试点	N61_RADIO_MLB	03/24/2014
31	32	BASEBAND_PMU(1 OF 2)	基带电源	N61_RADIO_MLB	03/24/2014
32	33	BASEBAND_PMU(2 OF 2)	基带电源	N61_RADIO_MLB	03/24/2014
33	34	BASEBAND(1 OF 2)	基带	N61_RADIO_MLB	03/24/2014
34	35	BASEBAND(1 OF 2)	基带	N61_RADIO_MLB	03/24/2014
35	36	MOBILE_DATA_MODEM(2 OF 2)	基带数据模式	N61_RADIO_MLB	03/24/2014
36	37	RF_TRANSCEIVER(1 OF 3)	射频	N61_RADIO_MLB	03/24/2014
37	38	RF_TRANSCEIVER(2 OF 3)	射频	N61_RADIO_MLB	03/24/2014
38	39	RF_TRANSCEIVER(3 OF 3)	射频	N61_RADIO_MLB	03/24/2014
39	40	QFE_DCDC	功放电源	N61_RADIO_MLB	03/24/2014
40	41	2G_PA	2G功放	N61_RADIO_MLB	03/24/2014
41	42	VERY_LOW_BAND_PAD	频段B13, B17, B28A, B28B功放	N61_RADIO_MLB	03/24/2014
42	43	LOW_BAND_PAD	频段B8, B20, B26功放	N61_RADIO_MLB	03/24/2014
43	44	MID_BAND_PAD	频段B1, B3, B4, B25, B34, B39功放	N61_RADIO_MLB	03/24/2014
44	45	HIGH_BAND_PAD	频段B27, B38, B40, B40A, B41B, B41C功放	N61_RADIO_MLB	03/24/2014
45	46	ANTENNA_SWITCH	天线开关	N61_RADIO_MLB	03/24/2014
46	47	HIGH_BAND_SWITCH	高频接收收发合路器	N61_RADIO_MLB	03/24/2014
47	48	RX_DIVERSITY	频段信号名称变更	N61_RADIO_MLB	03/24/2014
48	49	GPS	GPS	N61_RADIO_MLB	03/24/2014
49	50	GPS	GPS	N61_RADIO_MLB	03/24/2014
50	51	ANTENNA_FEEDS	天线接口	N61_RADIO_MLB	03/24/2014
51	52	WIFI/BT:MODULE_AND_FRONT_END	WIFI蓝牙	N61_RADIO_MLB	03/24/2014
52	53		移动支付NFC	N61_RADIO_MLB	03/24/2014
53	54	JUMPER	U5411_RF信号转换管	N61_RADIO_MLB	03/24/2014
54	55	JUMPER	U5501+U5502信号转换管	N61_RADIO_MLB	03/24/2014

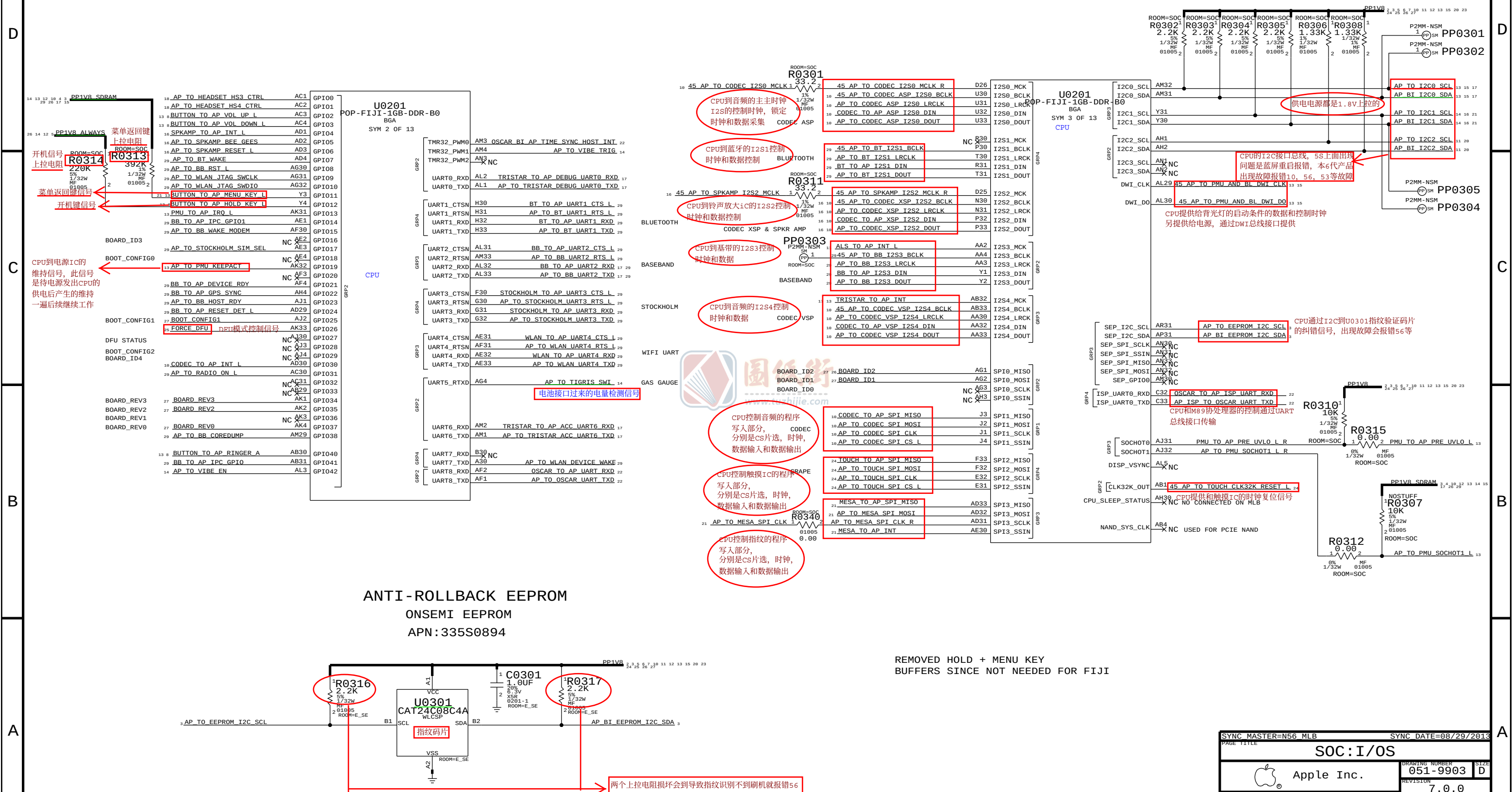
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BRD	820-3486
MCO	056-6825

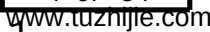
BOM 639-4237 (16GB, BETTER)
BOM 639-5838 (32GB, BEST)
BOM 639-5839 (64GB, ULTRA)

BOM 639-00208 (16GB, BETTER, DTD)
BOM 639-00209 (32GB, BEST, DTD)
BOM 639-00210 (64GB, ULTRA, DTD)

BOM 639-00025(128GB, SUPREME, TLC) BOM 639-00212(128GB, SUPREME, TLC, DTD)

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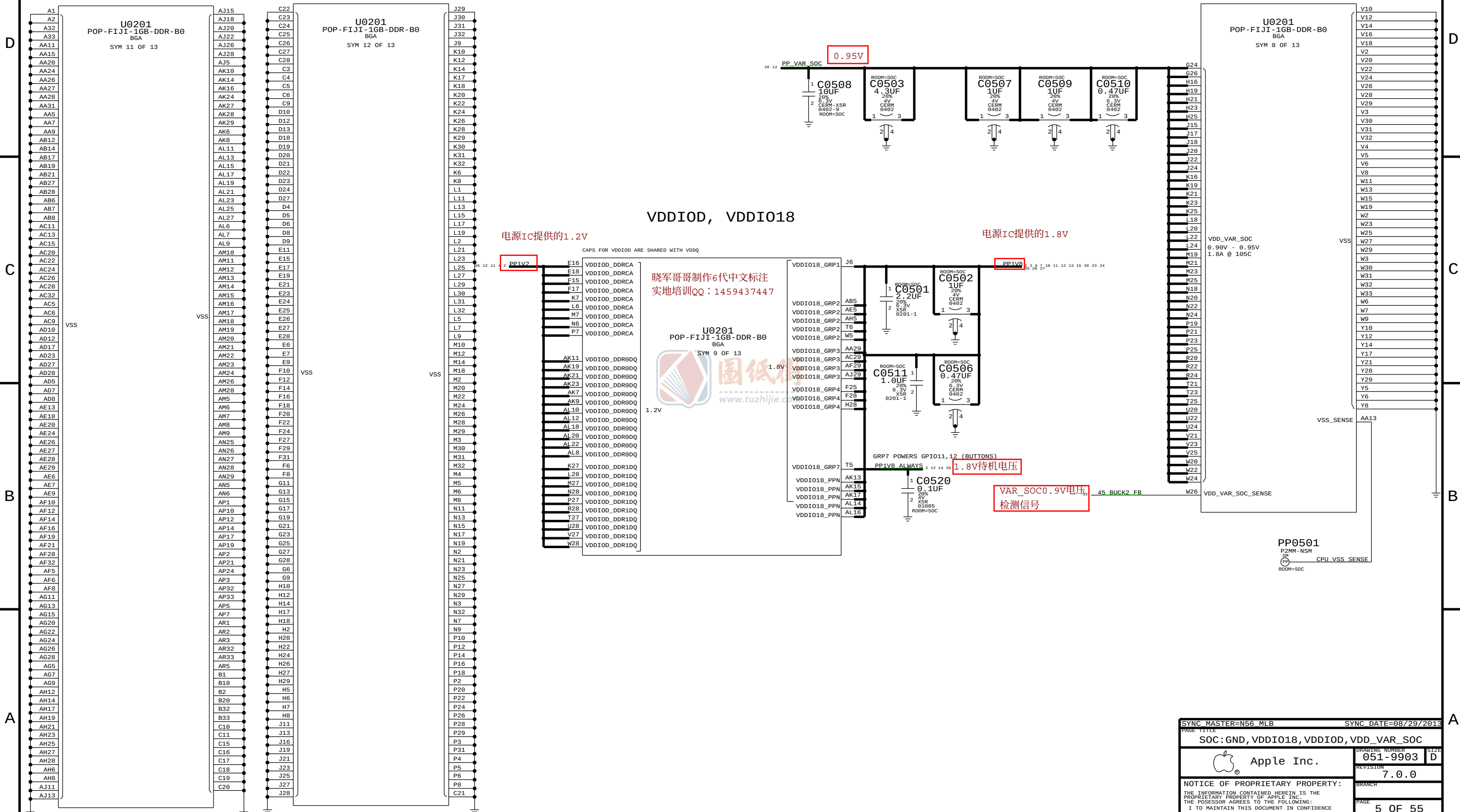




FIJI: VDDI0D, VDDI018, VDD_VAR_S0C

JUST A FEW GNDS

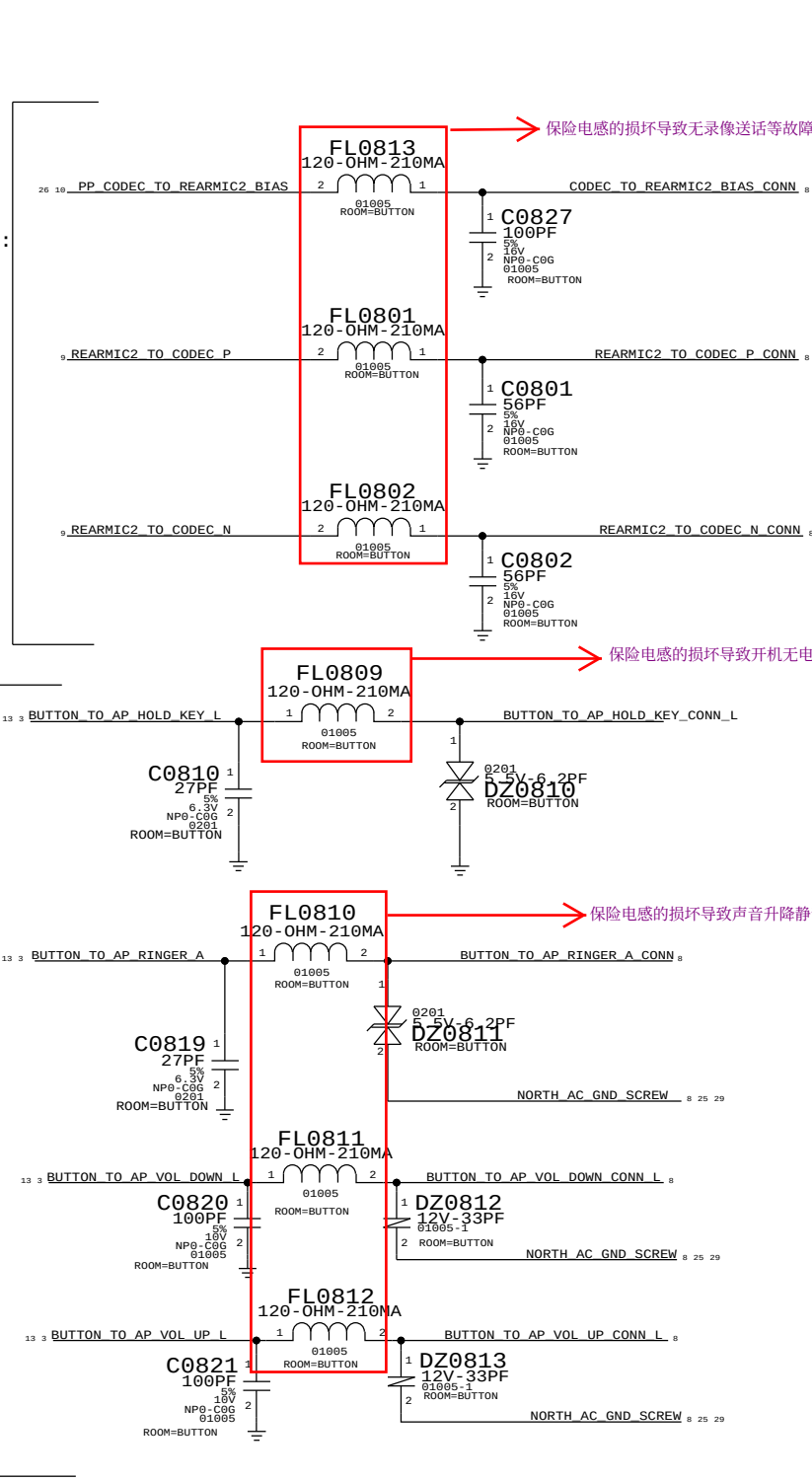
JUST A FEW GNDS



BUTTON FLEX (BUTTONS, ANC REF MIC, STROBE, STROBE_NTC, WIFI FLEX PAC)

MIC2 (ANC REF MIC):
MIC2/4 BIAS,
MIC2_P,_N

BUTTONS:
RINGER, HOLD,
VOL_UP/DOWN,

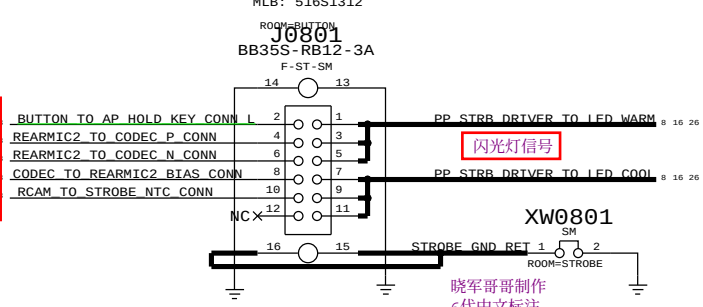


开机信号传送到电源IC和CPU
音频到接口的送话器信号
送话器的偏压信号
摄像头的温度检测信号

保险电感的损坏导致开机无电流反应

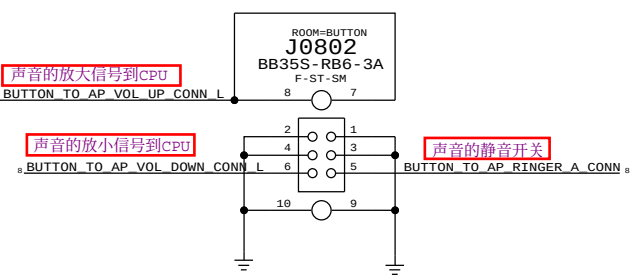
保险电感的损坏导致声音升降静音开关失效

RIGHT BUTTON B2B



闪光灯信号
晓军哥哥制作
6代中文标注
培训QQ:
1459437447

LEFT BUTTON B2B



声音的放大信号到CPU

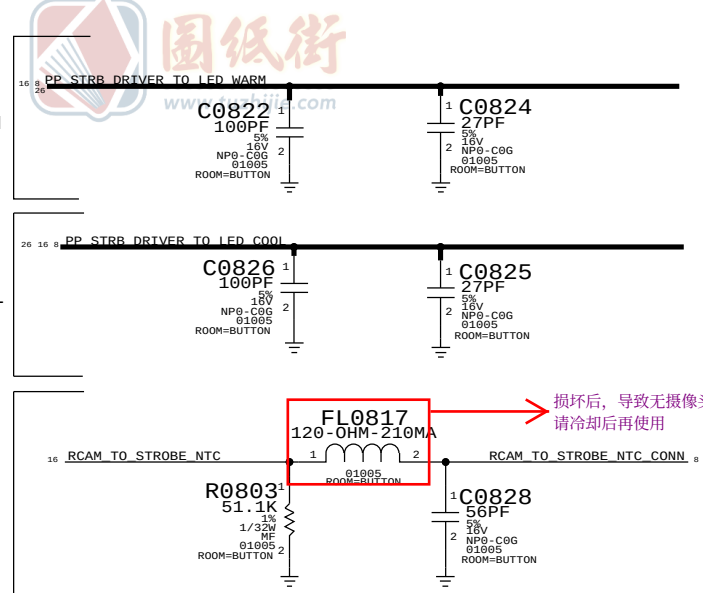
声音的微小信号到CPU

声音的静音开关

STROBE:
LED WARM

STROBE:
LED COOL

STROBE:
NTC



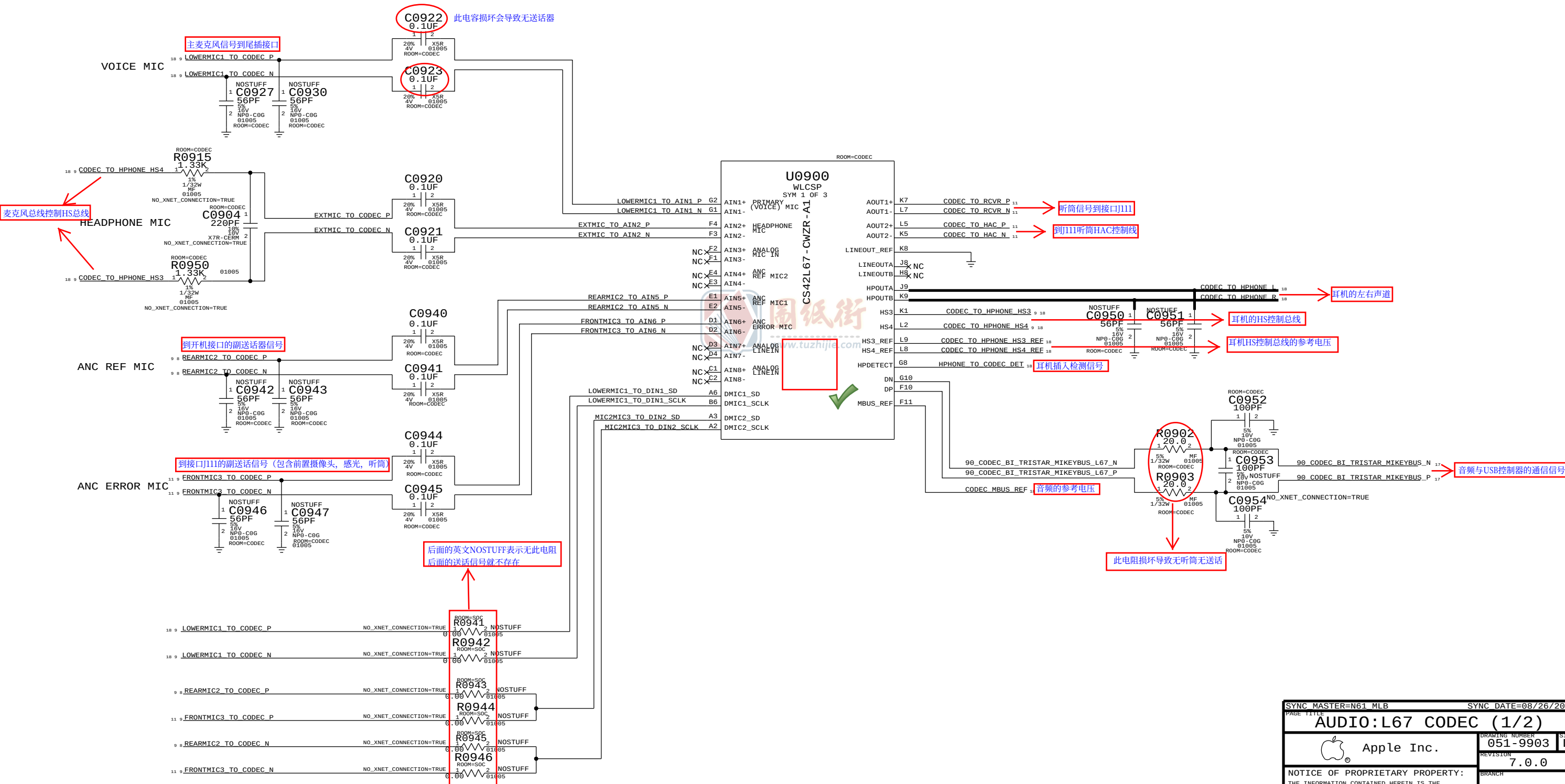
损坏后, 导致无摄像头温度检测, 开会会造成显示本机温度过高
请冷却后再使用

SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE			
IO:BUTTON FLEX CONN			
 Apple Inc.		DRAWING NUMBER	051-9903
		SIZE	D
		REVISION	7.0.0
		BRANCH	
		PAGE	8 OF 55
		SHEET	8 OF 54
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L67 AUDIO CODEC

AUDIO I/O

(ANALOG MIC IN, DIG MIC IN, HPOUT, LINEOUT, RECEIVER OUT, MIKEYBUS)



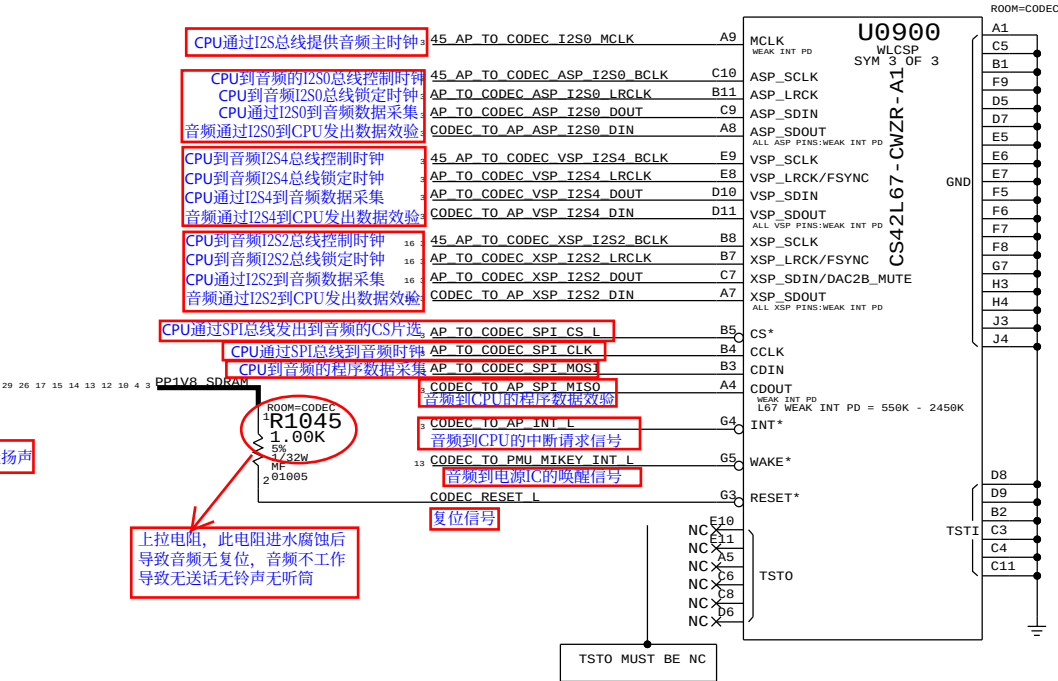
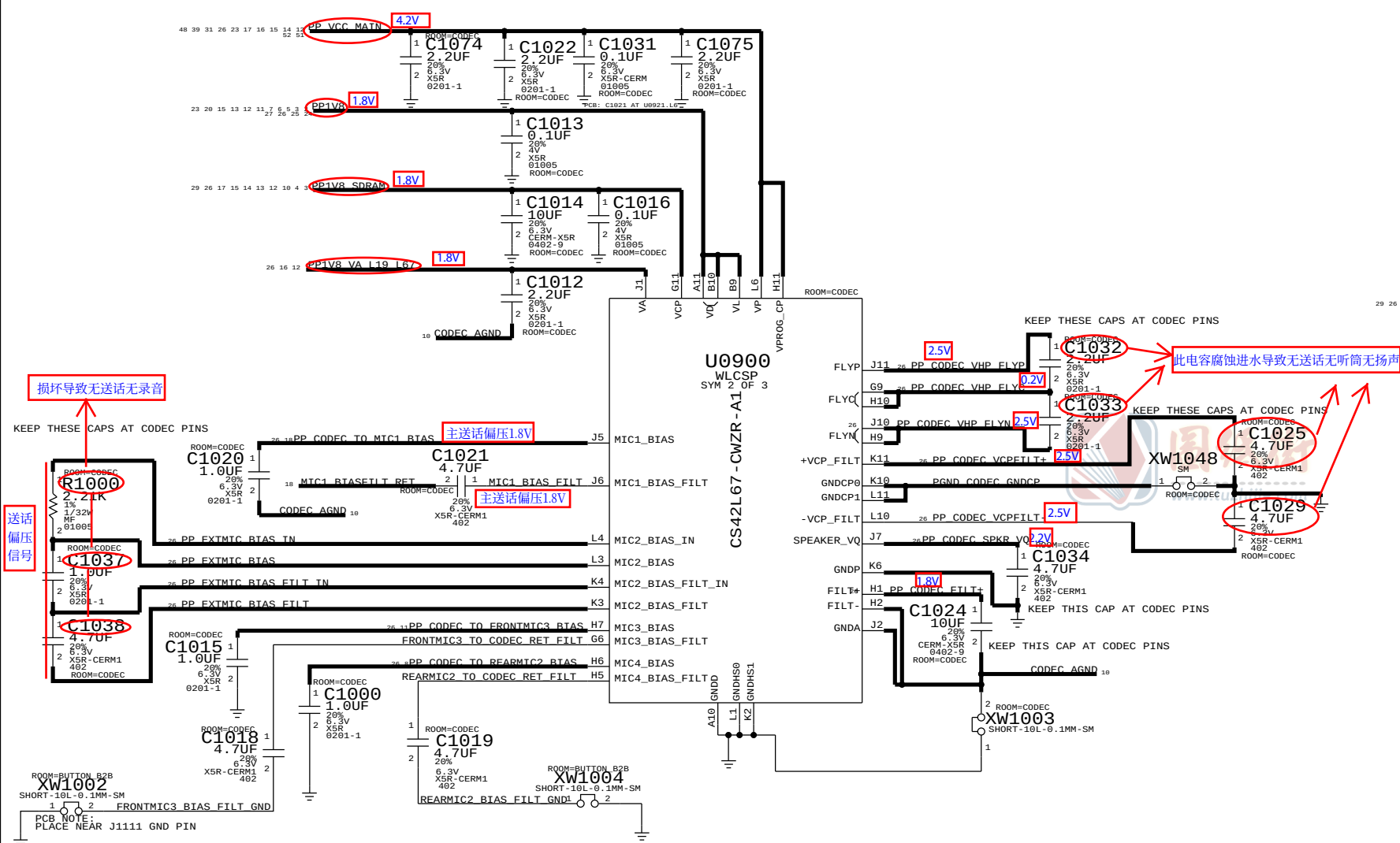
SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE			
AUDIO:L67 CODEC		(1/2)	
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L67 AUDIO CODEC

POWER, MICBIAS

DIGITAL SYSTEM I/O

NOTE: C1022 WAS REDUCED TO 2.2UF BECAUSE OF ADDITIONAL NEARBY VCC MAIN CAPS



SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE			
AUDIO:L67 CODEC		(2/2)	
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		PAGE	10 OF 55
		SHEET	10 OF 54

FRONT CAM FLEX B2B

(FCAM, PROX, ALS, RECEIVER, ANC ERROR MIC)

此保险电感损坏导致前置摄像头不照相

THIS ON ONE MLB ---> 516S1081 RECEPTACLE
SENSOR HOTBAR

J1111 感光, 听筒, 送话器, 前置摄像头接口

听筒HAC控制线

CODEC TO HAC CONN N

CODEC TO HAC CONN P

前置摄像头供电1.2V

90 FCAM TO AP MIPI DATA0 CONN N

90 FCAM TO AP MIPI DATA0 CONN P

90 FCAM TO AP MIPI CLK CONN N

90 FCAM TO AP MIPI CLK CONN P

90 FCAM TO AP MIPI DATA1 CONN N

90 FCAM TO AP MIPI DATA1 CONN P

前置摄像头高清显示数据和时钟信号

45 PROX TO CUMULUS RX CONN

ALS 3V供电

PP3V0 ALS CONN

ALS I2C2 SCL ALS CONN

ALS I2C2 SDA ALS CONN

感光3V供电

PP3V0 PROX TRILED

ALS I2C2 SCL ALS CONN

ALS I2C2 SDA ALS CONN

PP CODEC TO FRONTMIC3 BIAS CONN

FRONTMIC3 TO CODEC P CONN

FRONTMIC3 TO CODEC N CONN

FRONTMIC3 TO CODEC RX CONN

FRONTMIC3 TO CODEC TX CONN

FRONTMIC3 TO CODEC RX CONN

FRONTMIC3 TO CODEC TX CONN

FRONTMIC3 TO CODEC RX CONN

FRONTMIC3 TO CODEC TX CONN

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SYNC MASTER=N61 MLB SYNC DATE=08/26/2013

CAMERA: FRONT FLEX CONN

Apple Inc.

051-9903

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11 OF 55

11 OF 54

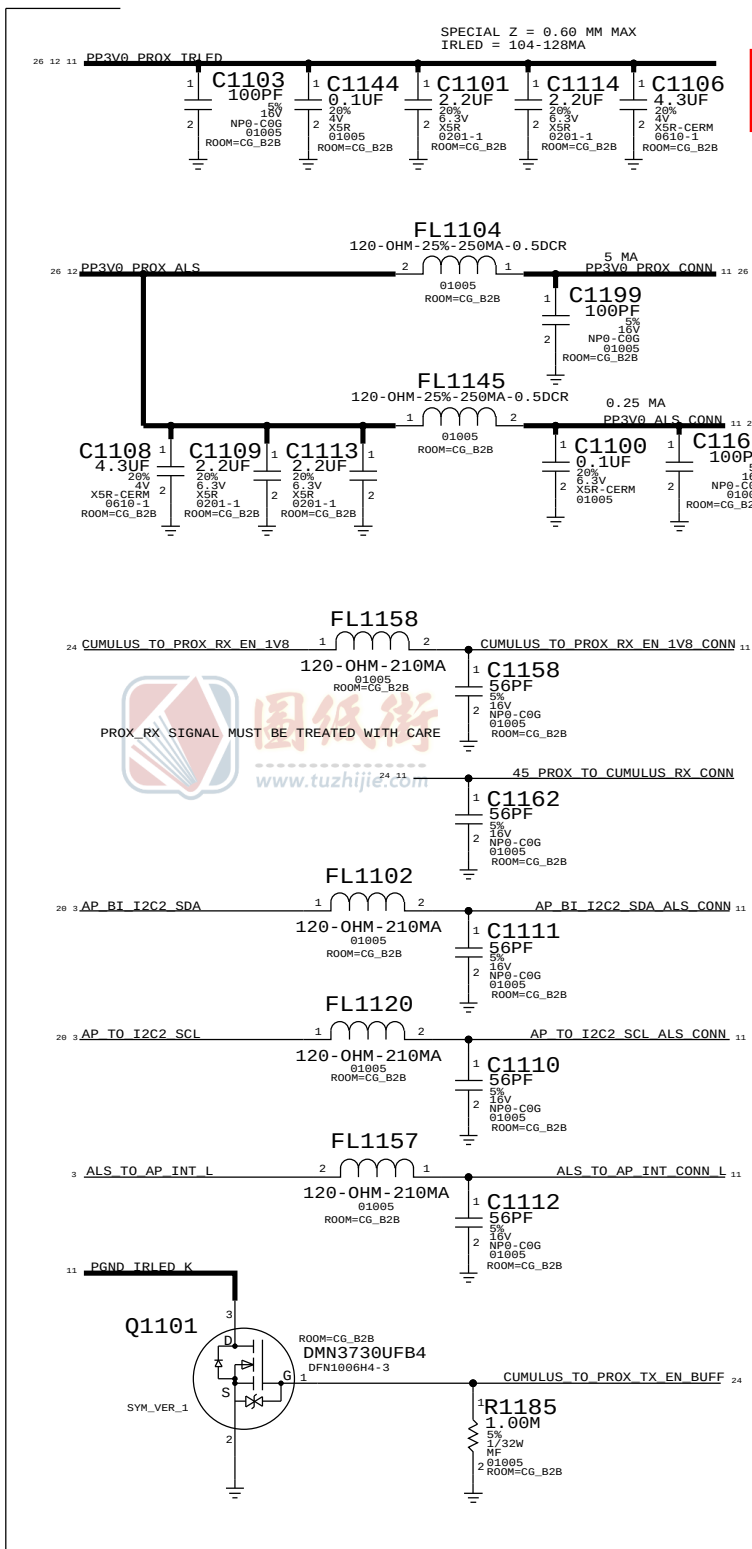
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ADI PMU

(BUCK, LDO, VIBE DRIVER, 32K, CHARGER)

NOTE: L1210, L1212 BOMPTIONS
CONTROLLED ON PAGE1

APN: 338S1251 (ADI AZ)

U1202
D2186AE0FJAVAC
FCCSP-N56-N61
SYM 1 OF 3

无此电阻
NOSTUFF
R1201
470
5%
1/32W
MF-1F
01005
ROOM=PMU

电池的电压检测
CHARGER_VBAT_SNS

PP_VCC_MAIN
B+主供电

DESENSE CAPS FOR VCCMAIN:
C1271: WIFI MODULE
C1272: AP PMU MODULE
BB PMU MODULE:RF SIDE
QPOET MODULE:RF SIDE

C1271 100PF
NP0-C06
10V
01005
ROOM=WIFI

C1272 100PF
NP0-C06
10V
01005
ROOM=PMU

1.2V
PP1V2_SDRAM
C1278
2.2UF
6.3V
X5R
0201-1
ROOM=PMU

ROOM=PMU
Y1200
32.768K-20PPM-12.5PF
2.0X1.2X0.60-SM1
ROOM=PMU
C1276
18PF
10V
X5R
01005

SPEC REQUIRES 10NF,
VPUMP RUNS AT 4.6V
VPUMP CAP:
30% DERATED.

(50MA) VLD01
F18 2.5-3.3V +/-77.5MV
(50MA) VLD02
R14 1.2-1.9V +/-42.5MV
(50MA) VLD03
G18 2.5-3.3V +/-75MV
(50MA) VLD04
H18 2.5-3.6V +/-75MV
(100MA) VLD05
R15 2.5-3.6V +/-75MV
(150MA) VLD06
R13 1.2-3.6V +/-82.5MV
(250MA) VLD07
K18 2.5-3.6V +/-75MV
(250MA) VLD08
L18 2.5-3.6V +/-75MV
(250MA) VLD09
R10 2.5-3.6V +/-71.25MV
VLD09_FB
P10 PP2V9_LDO9
VLD010
L1 6.5-3.4V +/-25MV
(100MA) VLD010
L1 6.5-3.4V +/-25MV
(250MA) VLD011
R11 2.5-3.6V +/-82.5MV
(5MA) VLD012
L6 FIXED 1.8V +/-5%
(250MA) VLD013
J18 2.5-3.6V +/-71.25MV

VPUMP

C1208 0.1UF
2 6.3V
X5R-CERM
0201-1
ROOM=PMU
C1270 2.2UF
2 6.3V
X5R
0201-1
ROOM=PMU
C1229 2.2UF
2 6.3V
X5R
0201-1
ROOM=PMU
C1212 1.0UF
2 6.3V
X5R
0201-1
ROOM=PMU
C1284 1.0UF
2 6.3V
X5R
0201-1
ROOM=PMU
C1299 2.2UF
2 6.3V
X5R
0201-1
ROOM=PMU
C1207 2.2UF
2 6.3V
X5R
0201-1
ROOM=PMU

到USB控制器的3.3V
到音频电路的1.8V
到USB控制器的3V
到M8协处理器电路的3V
到使能的3V
到USB控制器的3V
到感光的3V

到CPU的 1v
到感光的 3v
到指纹IC的 3v

PP3V3 USB
PP1V8 VA I19 I67
PP3V0 TRISTAR
PP3V0 TML
PP3V0 NAND
PP3V3 ACC
PP3V0 PROX ALS

TO DO: REVIEW ALL LDO ASSIGNMENTS
(CHECK VDD_FDD INPUT SOURCE)
VS- LOAD REQUIREMENT AT DESTINATION, ETC)
SPEAKER AMP, CODEC VA (2.5 MA L1419, 3MA L67)
TRISTAR VDH, WIFI_FLEX PAC (? MA)
GYRO, ACCEL, COMPASS (? MA) NOTE: 3V +/- 5% PER EUGENE
NAND (? MA)
ACCESSORY POWER (? MA)
PROX/ALS VDD (PROX: 0.75/1.2 MA ALS: 0.175/0.25 MA [TYP/MAX])
REAR CAM AUTO FOCUS (120MA PEAK, PROBABLY CAP AT 80MA)
REAR/FRONT CAM AVDD (? MA)
SOC IVO MIPI, USB_DVDD, DP (71 MA TOTAL)
PROX LED (102 MA TYP)
ALWAYS ON 1V8 (? MA)

SYNC MASTER=N56 MLB SYNC DATE=08/29/2013

PAGE TITLE
POWER:ADI (1/2)

Apple Inc.

DRAWING NUMBER
051-9903

REVISION
7.0.0

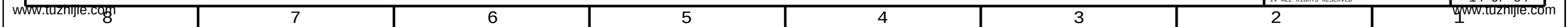
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12 OF 55

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12 OF 54

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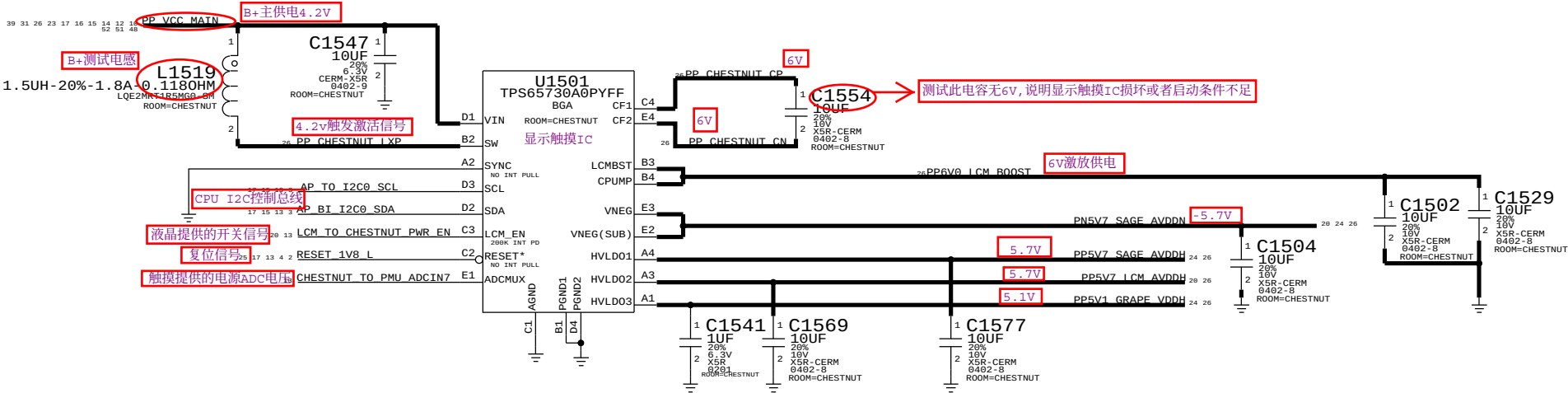
充电电源IC和震动马达IC



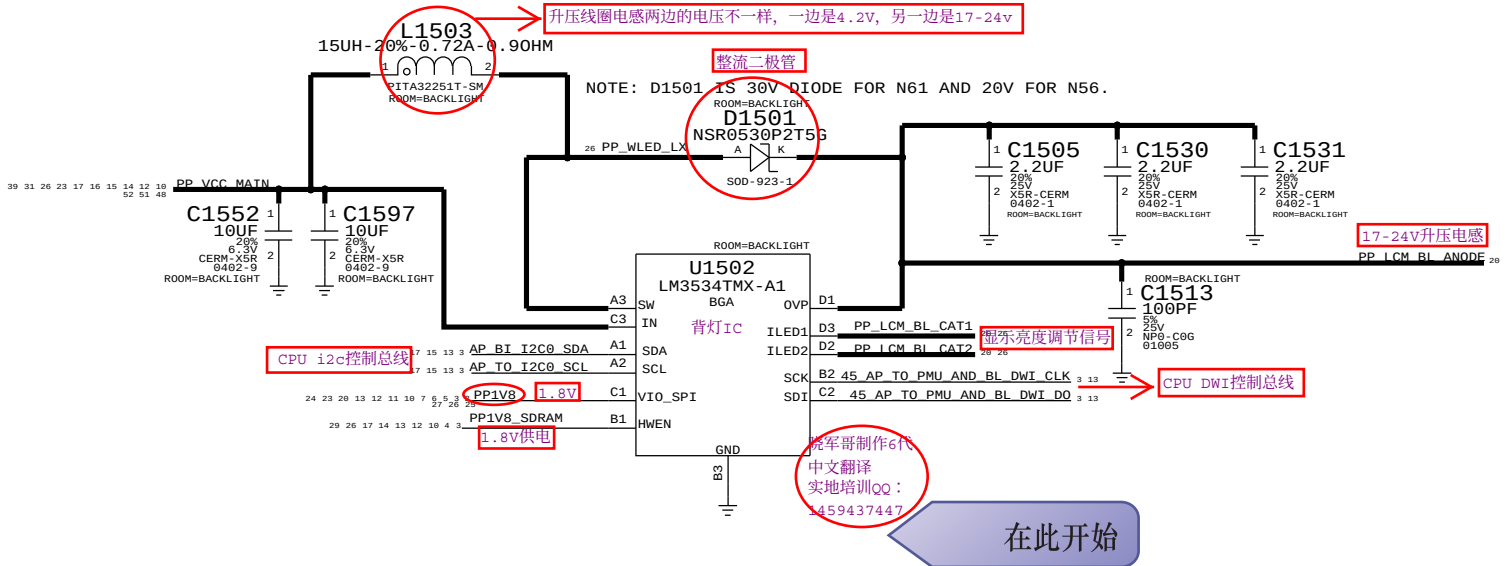
CHESTNUT, BACKLIGHT DRIVER, MESA BOOST

显示触摸IC+背灯IC+指纹IC

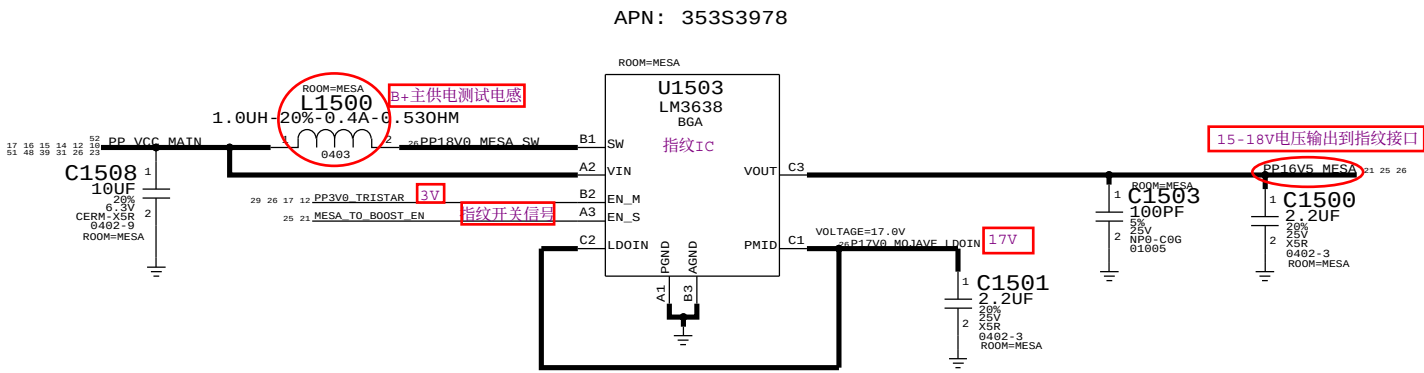
D500 DISPLAY PMU (TI CHESTNUT, 338S1149)



D500 BACKLIGHT DRIVER



MESA BOOST A0

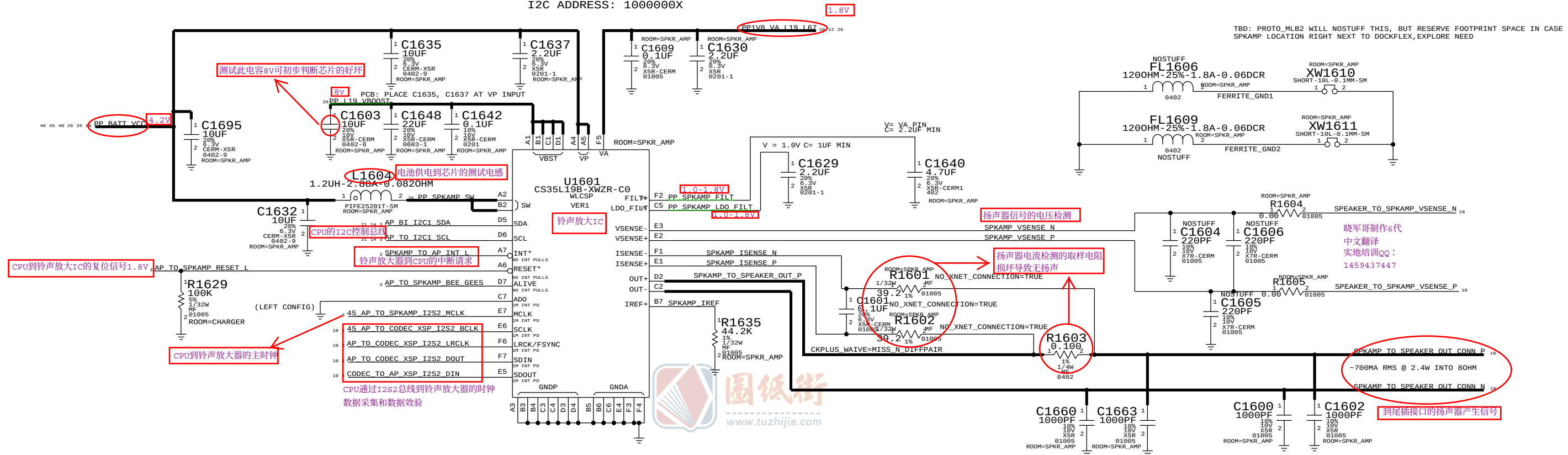


SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE		DISPLAY:CHESTNUT, BACKLIGHT DRIVER	
Apple Inc.		DRAWING NUMBER	051-9903
		REVISION	7.0.0
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SPEAKER AMP, LED DRIVER

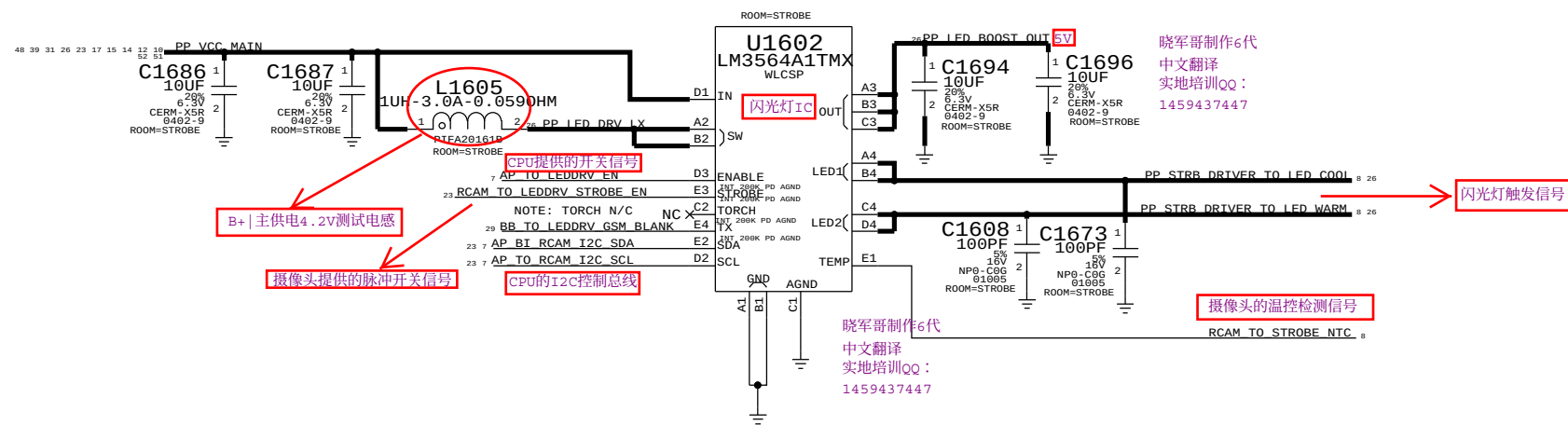
SPEAKER AMP

I2C ADDRESS: 1000000X



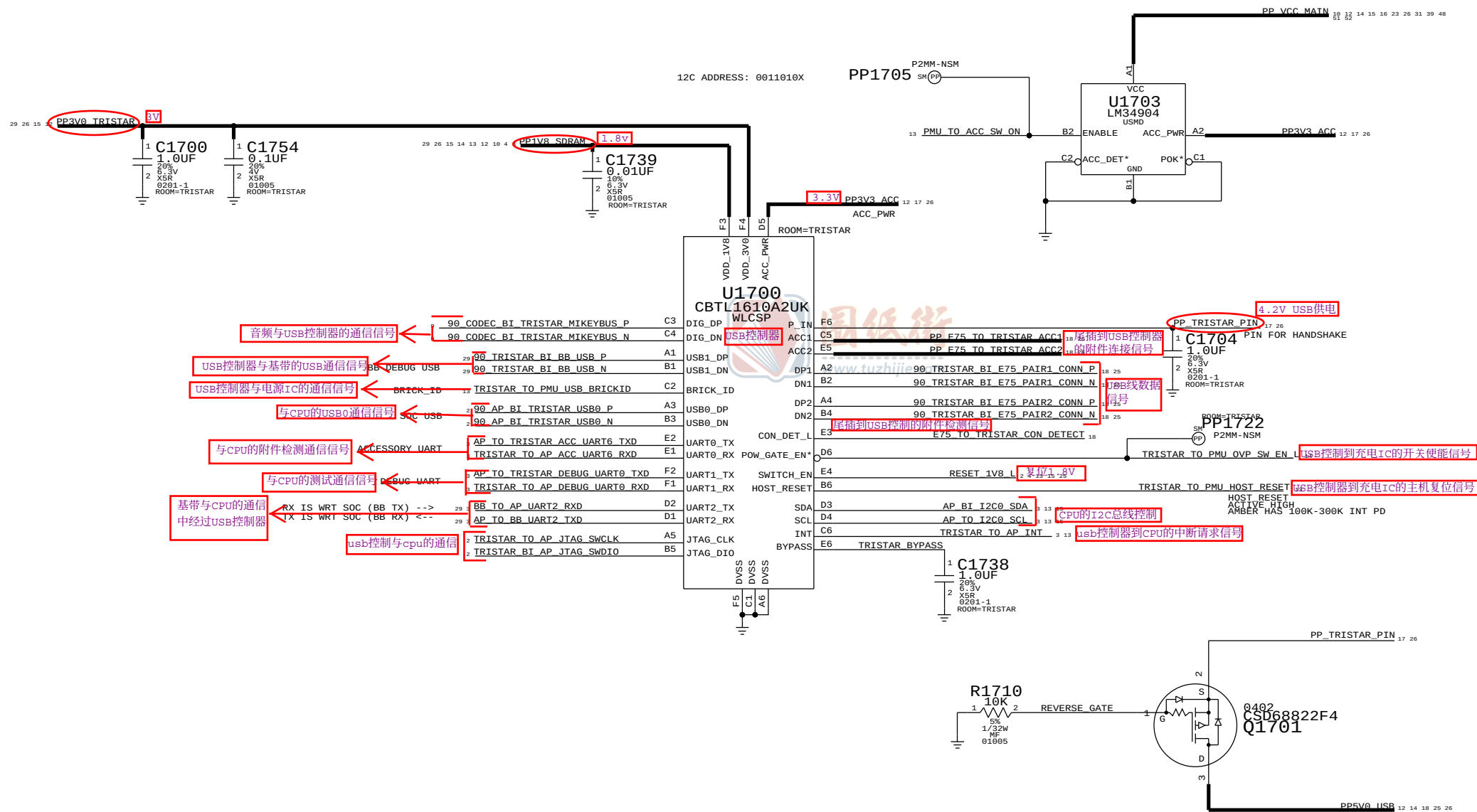
STROBE DRIVER

TI: APN 353S3899



SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE		AUDIO: SPKR AMP, STROBE	
Apple Inc.		DRAWING NUMBER	051-9903
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TRISTAR2



SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE			
IO:TRISTAR2			
 Apple Inc.		DRAWING NUMBER	051-9903
		SIZE	D
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		BRANCH	
		PAGE	17 OF 55
		SHEET	17 OF 54

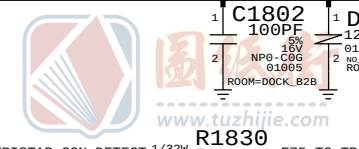
D LOWER MIC1
(PRIMARY
VOICE MIC)



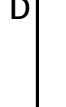
SPEAKER:
LEADS,
VSENSE

B

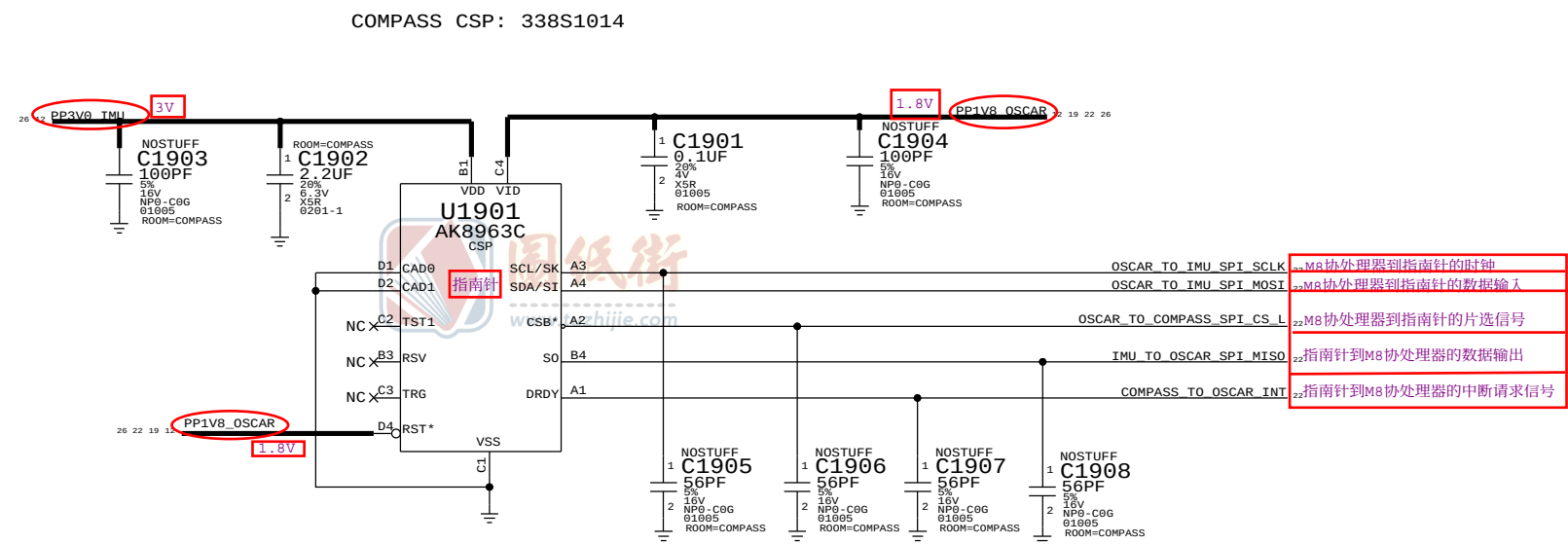
A



5V充电电压到充电IC ← 26 2

C

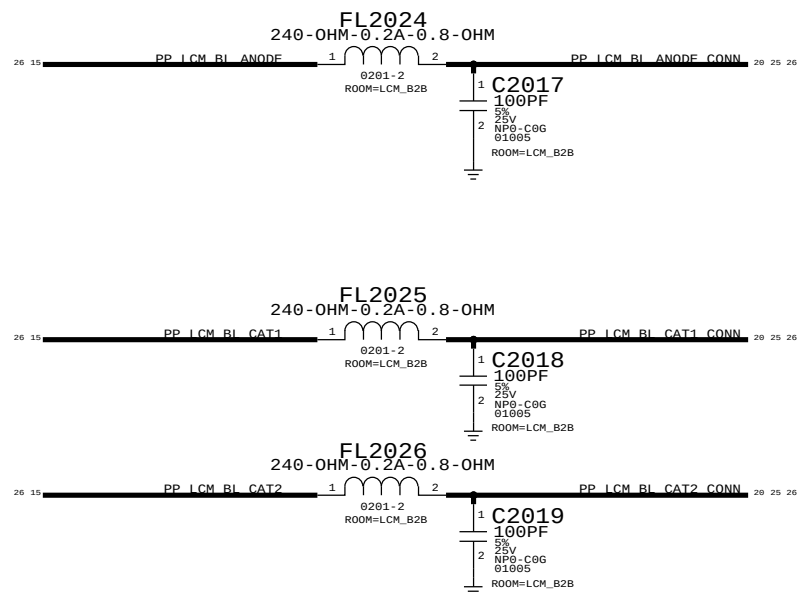
COMPASS - AKM COMPASS IN POR LOCATION



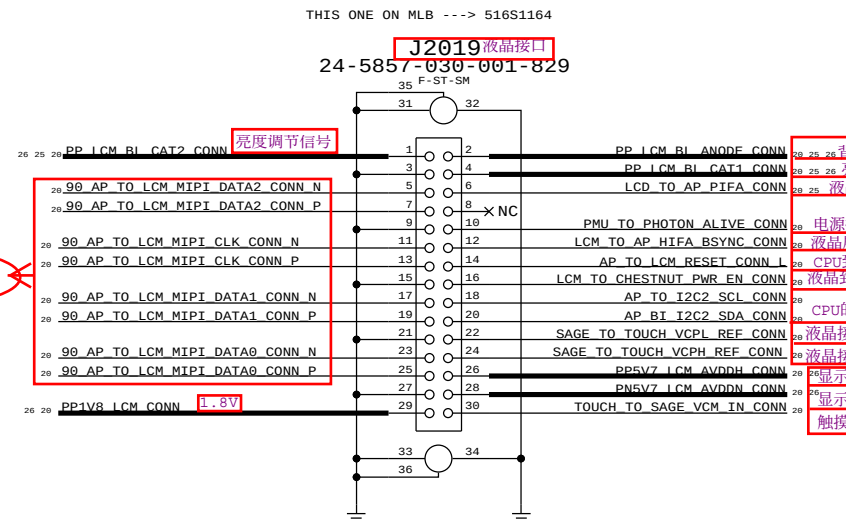
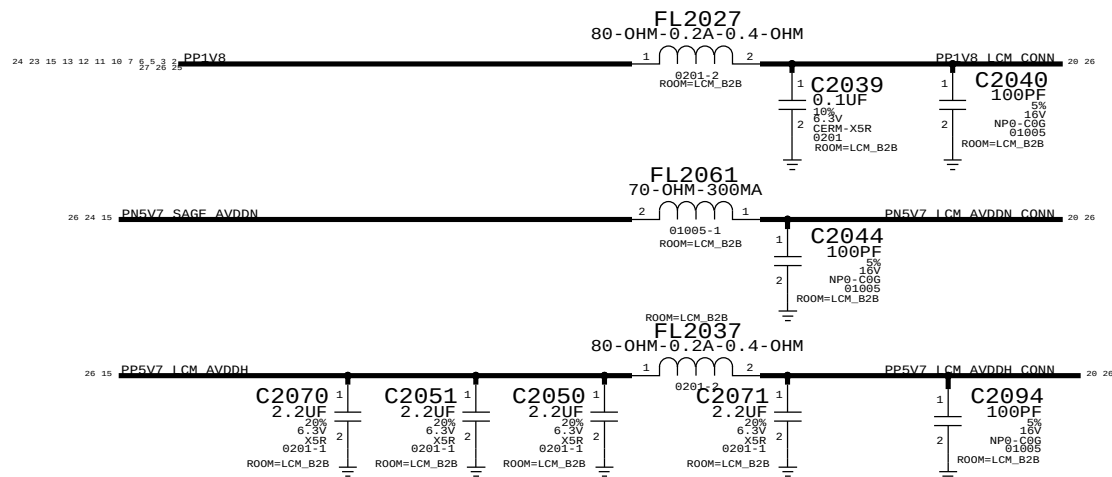
SYNC_MASTER=N61_MLB		SYNC_DATE=08/26/2013	
PAGE TITLE			
SENSORS : COMPASS			
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		PAGE	19 OF 55
		SHEET	19 OF 54
		SIZE	D

LCD B2B

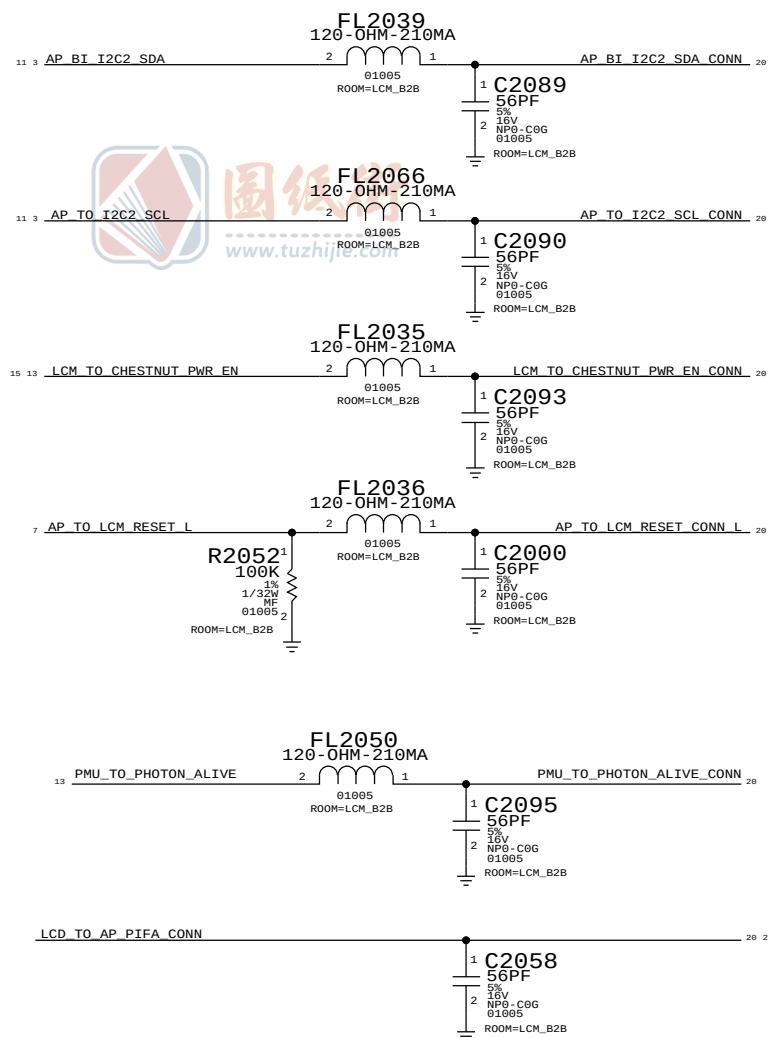
Backlight
(N56 HAS A 2ND SET OF BL SIGNALS ON P. 19).



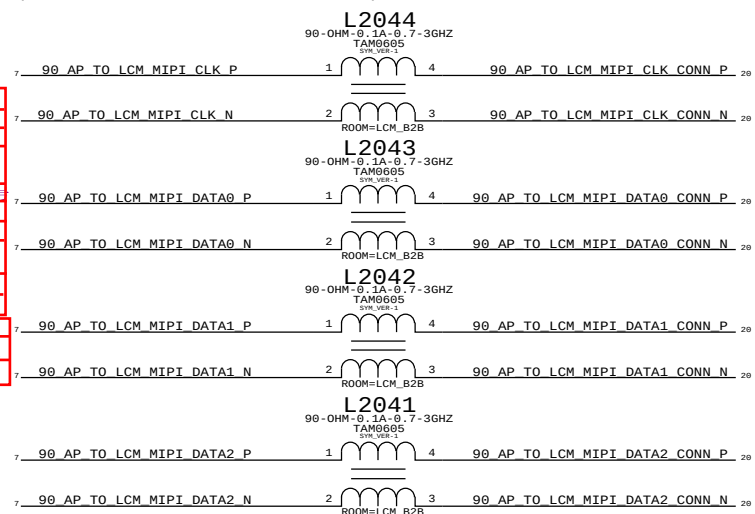
LCM Supplies



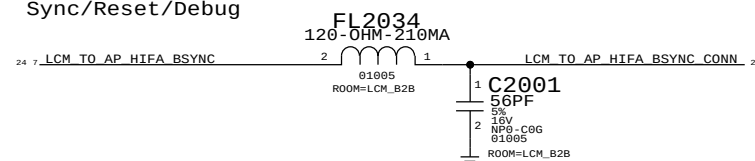
Digital Interfaces



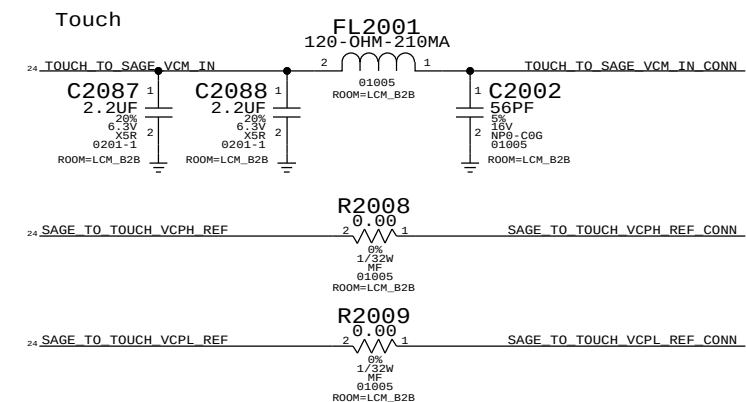
MIPI Common Mode Chokes
(N56 HAS A 4TH MIPI LANE ON P. 19).



Sync/Reset/Debug



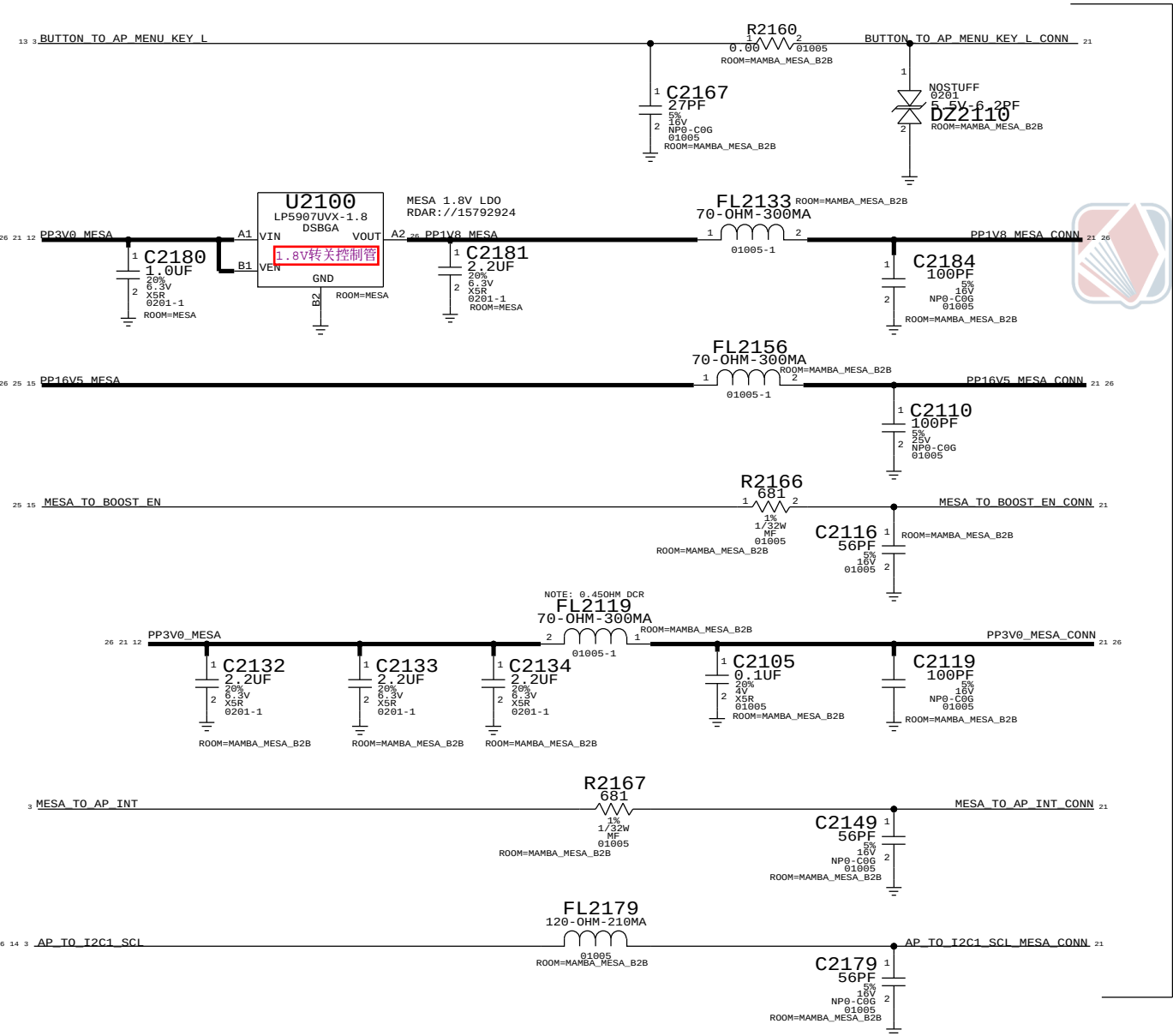
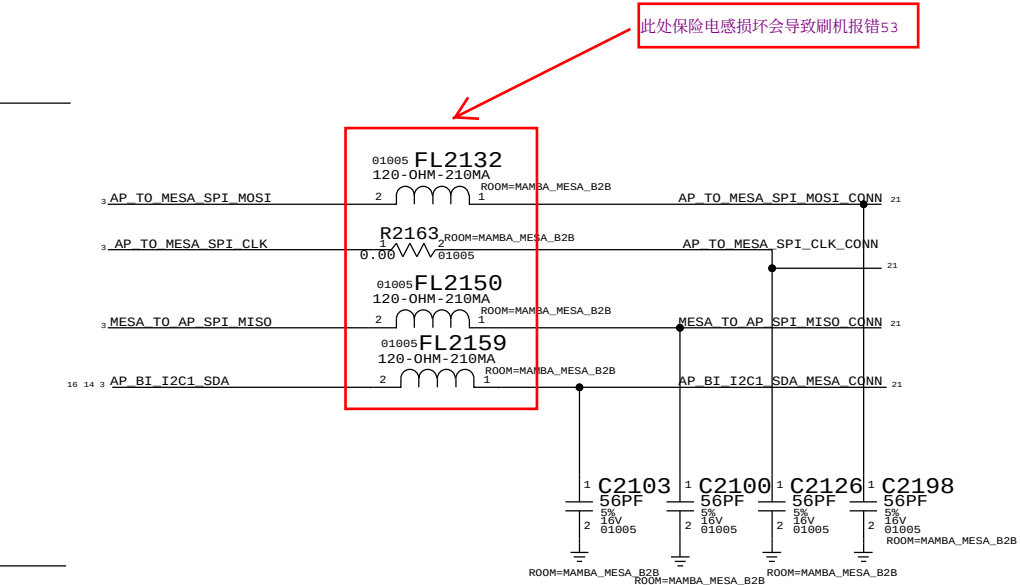
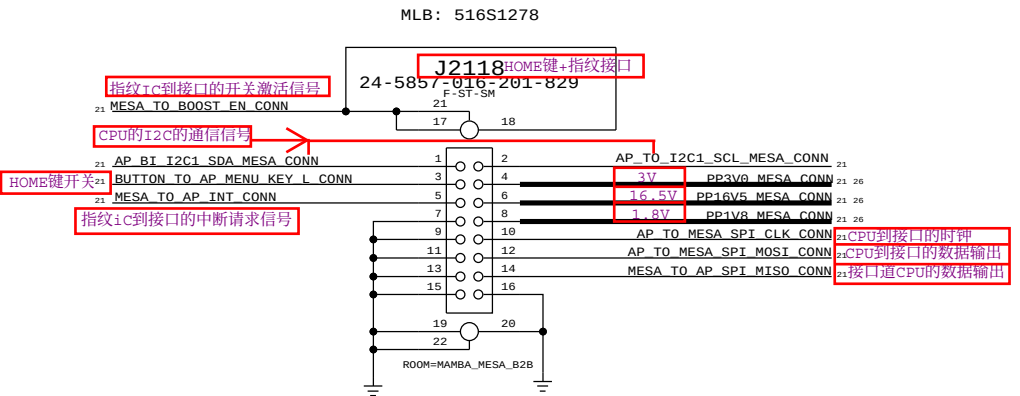
Touch



SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE		DISPLAY:FLEX CONN	
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MESA CONNECTOR

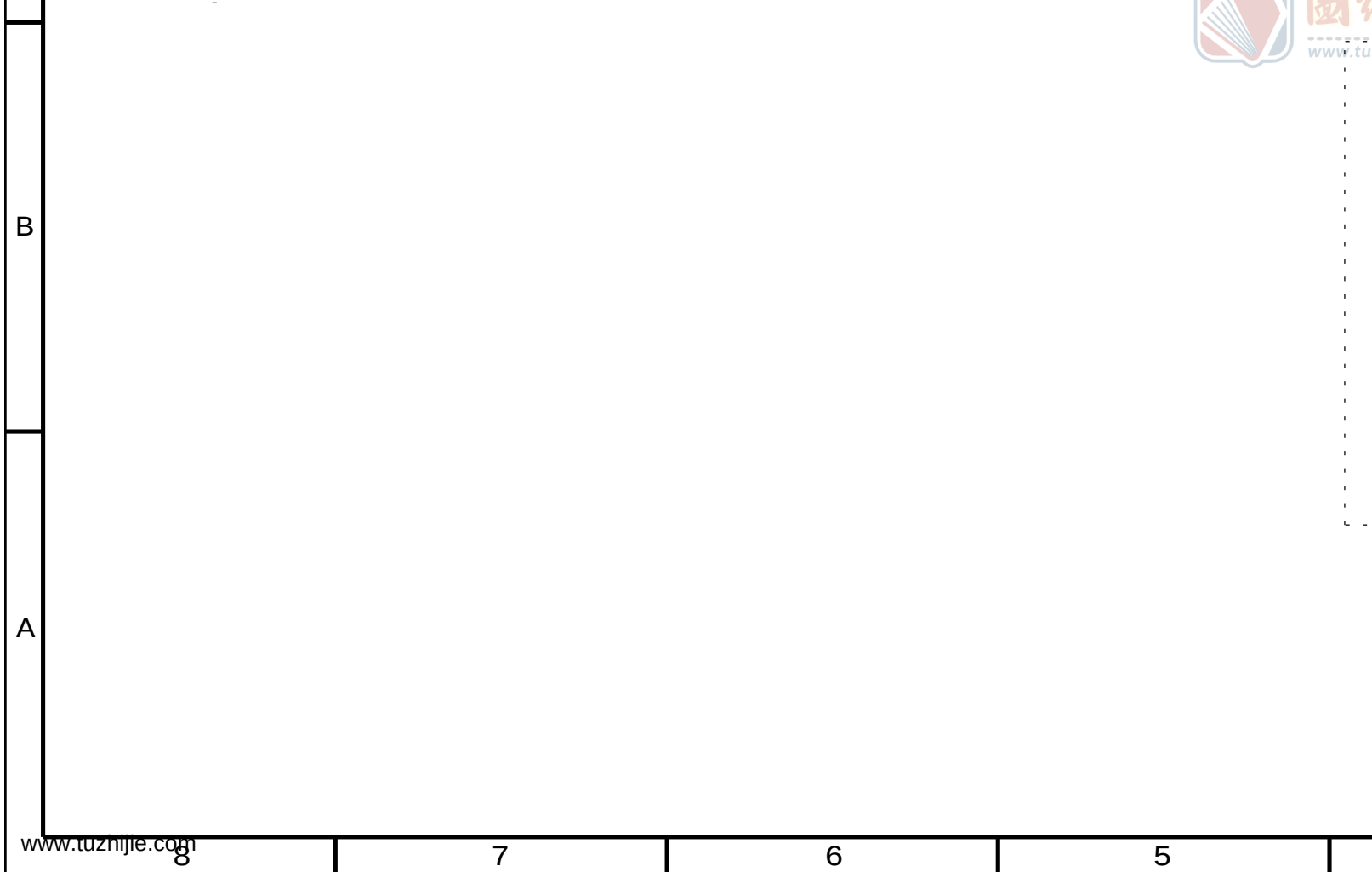
接口无供电或者任何一个信号
出现问题都会刷机报错53



MESA SENSOR:
www.tuzhijie.com

PAGE TITLE		
SENSORS:MESA FLEX CONN		
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26 22 19 17 PP1V8 OSCAR 1.8V

[illegible]

SYMC MASTER-N61 MLB		SYMC DATE=08/26/2013	
PAGE TITLE		PAGE	
SENSORS: OSCAR, CARBON, PHOS, MAGNESIUM			
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BRANCH		PAGE	
		22 OF 55	
SHEET			
		22 OF 54	

RCAM B2B (REAR CAMERA CONNECTOR)

摄像头到CPU的
显示成像信号

RCAM:
4-LANE MIPI

摄像头与CPU的显示信号的检测电感
损坏导致不照相
报错56等故障

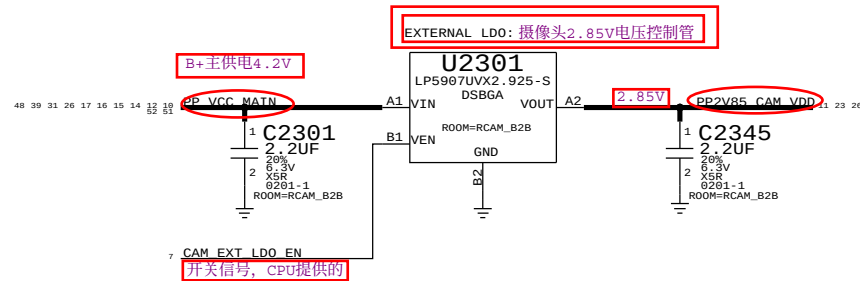
RCAM:
DIGITAL I/F
(I2C, CTRL, CLK)

此处保险电感非常容易断线
断线后导致不照相和刷机报错56
可从前置摄像头出飞线过来即可

RCAM:
POWER:
(1.8V DVDD)
(2.8V AVDD)
(1.2V VCC)
(1.8V/2V AF)



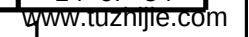
RCAM/FCAM AVDD RAIL EXT. LDO:



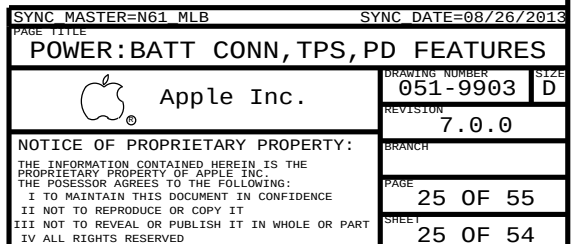
SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
CAMERA: REAR FLEX CONN		DRAWING NUMBER	051-9903
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		PAGE	23 OF 55
		SHEET	23 OF 54

APN: 343S0694

APN: 343S0638



A



此页是电压综合处

VOLTAGE PROPERTIES

1E55	VOLTAGE=3.3V	PP3V3_USB	2 12
1E56	VOLTAGE=1.8V	PP1V8_VA_I19_I67	10 12 16
1E57	VOLTAGE=3.0V	PP3V0_TRISTAR	12 16 17 29
1E58	VOLTAGE=3.0V	PP3V0_TMIJ	12 19
1E59	VOLTAGE=3.0V	PP3V0_NAND	6 12
1E60	VOLTAGE=3.0V	PP3V3_ACC	12 17
1E61	VOLTAGE=3.0V	PP3V0_PROX_ALS	11 12
1E64	VOLTAGE=4.6V	PP_VCC_MAIN	18 12 14 15 16 17 23 31 39
1E65	VOLTAGE=1.0V	PP1V0	7 12
1E66	VOLTAGE=3.0V	PP3V0_PROX_TRIED	11 12
1E67	VOLTAGE=1.8V	PP1V8_ALWAYS	1 5 12 14
1E68	VOLTAGE=3.0V	PP3V0_MESA	12 21
1E69	VOLTAGE=1.1V	PP_CPU	4 12
1E70	VOLTAGE=1.1V	PP_GPU	4 12
1E71	VOLTAGE=1.2V	PP1V2_SDRAM	2 4 12 23
1E72	VOLTAGE=1.8V	PP1V8_SDRAM	3 4 10 12 13 14 15 17 29
1E73	VOLTAGE=1.8V	PP1V8	2 4 5 7 10 11 12 13 15 20 23
1E74	VOLTAGE=1.8V	PP1V8_GRAPE	12 24
1E75	VOLTAGE=1.8V	PP1V8_OSCAR	12 19 22
1E76	VOLTAGE=1.2V	PP1V2_NAND_VDDT	6
1E7A	VOLTAGE=1.8V	PP_EXTMTC_BIAS_FTILT_IN	10
1E7B	VOLTAGE=1.8V	BOARD_ID2	3 27
1E7C	VOLTAGE=1.2V	PP1V2	4 5 11 12
1E80	VOLTAGE=5.0V	PP_E75_TO_TRISTAR_ACC1_CONN	18 25
1E81	VOLTAGE=5.0V	PP_E75_TO_TRISTAR_ACC1	17 18
1E82	VOLTAGE=22.0V	PP_LCM_BI_ANODE	15 20
1E83	VOLTAGE=0.2V	PP_LCM_BI_CAT2	15 20
1E84	VOLTAGE=0.2V	PP_LCM_BI_CAT1	15 20
1E85	VOLTAGE=0.2V	PP_LCM_BI_CAT2_CONN	20 25
1E86	VOLTAGE=0.2V	PP_LCM_BI_CAT1_CONN	20 25
1E8D	VOLTAGE=-5.7V	PP5V7_SAGE_AVDDN	15 20 24
1E8E	VOLTAGE=1.2V	PP1V2_OSCAR	12 22
1E8F	VOLTAGE=3.0V	PP3V0_MESA_CONN	23
1E90	VOLTAGE=6V	PP6V0_LCM_BOOST	15
1E91	VOLTAGE=5.0V	PP_STRB_DRIVER_TO_LED_WARM	8 16
1E92	VOLTAGE=5.0V	PP_STRB_DRIVER_TO_LED_COOL	8 16
1E93	VOLTAGE=1.8V	PP_CODEC_TO_MIC1_BIAS	10 18
1E94	VOLTAGE=1.8V	PP_EXTMTC_BIAS_FTILT_IN	10
1E95	VOLTAGE=1.8V	PP_EXTMTC_BIAS_FTILT	10
1E96	VOLTAGE=1.8V	PP_CODEC_TO_FRONTMTC3_BIAS	10 11
1E97	VOLTAGE=1.8V	PP_CODEC_TO_REARMTC2_BIAS	8 10
1E98	VOLTAGE=1.8V	PP_CODEC_FTILT+	10
1E99	VOLTAGE=2.2V	PP_CODEC_SPKR_V0	10
1E9A	VOLTAGE=2.5V	PP_CODEC_VCPETILT-	10
1E9B	VOLTAGE=2.5V	PP_CODEC_VCPETILT+	10
1E9C	VOLTAGE=2.5V	PP_CODEC_VHP_ELYN	10
1E9D	VOLTAGE=0.2V	PP_CODEC_VHP_ELYC	10
1E9E	VOLTAGE=2.5V	PP_CODEC_VHP_ELYP	10
1E9F	VOLTAGE=1.8V	PP1V8_ECAM_CONN	11
1E99	VOLTAGE=3.0V	PP2V85_ECAM_AVDD_CONN	11
1E9A	VOLTAGE=1.8V	PP_CODEC_TO_FRONTMTC3_BIAS_CONN	11
1E9B	VOLTAGE=3.0V	PP3V0_ALS_CONN	11
1E9C	VOLTAGE=1.2V	PP1V2_ECAM_VDDIO_CONN	11
1E9D	VOLTAGE=5.0V	PP5V0_USB	12 14 17 18 25
1E9E	VOLTAGE=5.0V	PP5V0_USB_TO_PMIJ	12
1E9F	VOLTAGE=4.6V	PP_BUCK5_LX0	12
1E99	VOLTAGE=4.6V	PP_BUCK3_LX	12
1E9A	VOLTAGE=4.6V	PP_BUCK4_LX	12
1E9B	VOLTAGE=4.6V	PP_BUCK2_LX	12
1E9C	VOLTAGE=4.6V	PP_BUCK1_LX1	12
1E9D	VOLTAGE=4.6V	PP_BUCK1_LX0	12
1E9E	VOLTAGE=4.6V	PP_BUCK0_LX3	12
1E9F	VOLTAGE=4.6V	PP_BUCK0_LX2	12
1E99	VOLTAGE=4.6V	PP_BUCK0_LX1	12
1E9A	VOLTAGE=4.6V	PP_BUCK0_LX0	12
1E9B	VOLTAGE=6.0V	PP_CHESTNUT_LXP	15
1E9C	VOLTAGE=6.0V	PP_CHESTNUT_CP	15
1E9D	VOLTAGE=6.0V	PP_CHESTNUT_CN	15
1E9E	VOLTAGE=5.7V	PP5V7_SAGE_AVDDH	15 24
1E9F	VOLTAGE=5.7V	PP5V7_LCM_AVDDH	15 24
1E99	VOLTAGE=5.1V	PP5V1_GRAPE_VDDH	15 24
1E9A	VOLTAGE=22.0V	PP_WLED_LX	15
1E9B	VOLTAGE=18.0V	PP18V0_MESA_SW	15
1E9C	VOLTAGE=17.0V	PP17V0_MOJAVE_IROUTN	15
1E9D	VOLTAGE=16.5V	PP16V5_MESA	15 21 25
1E9E	VOLTAGE=8.0V	PP_SPKAMP_SW	16
1E9F	VOLTAGE=8.0V	PP_I19_VB00ST	16
1E99	VOLTAGE=1.8V	PP_SPKAMP_FTILT	16
1E9A	VOLTAGE=1.8V	PP_SPKAMP_I1DO_FTILT	16
1E55	VOLTAGE=5.0V	PP_LED_DRV_LX	16
1E56	VOLTAGE=5.0V	PP_LED_BOOST_OUT	16
1E57	VOLTAGE=2.9V	PP2V9_I1DO9	12
1E58	VOLTAGE=1.8V	PP_CODEC_TO_MIC1_BIAS_CONN	18
1E59	VOLTAGE=4.6V	PP_E75_TO_TRISTAR_ACC2	17 18
1E5A	VOLTAGE=4.6V	PP_E75_TO_TRISTAR_ACC2_CONN	18 25
1E5B	VOLTAGE=1.8V	PP1V8_LCM_CONN	20
1E5C	VOLTAGE=22.0V	PP_LCM_BI_ANODE_CONN	20 25
1E5D	VOLTAGE=-5.7V	PP5V7_LCM_AVDDN_CONN	20
1E5E	VOLTAGE=5.7V	PP5V7_LCM_AVDDH_CONN	20
1E5F	VOLTAGE=1.8V	PP1V8_MESA	21
1E60	VOLTAGE=16.5V	PP16V5_MESA_CONN	21
1E61	VOLTAGE=5.0V	PP_TRISTAR_PIN	17
1E62	VOLTAGE=1.2V	PP1V2_RCAM_CONN	23
1E63	VOLTAGE=1.8V	PP1V8_RCAM_CONN	23
1E64	VOLTAGE=3.0V	PP2V85_CAM_VDD	23 25
1E65	VOLTAGE=1.8V	PP2V85_RCAM_AVDD_CONN	23
1E66	VOLTAGE=1.8V	PP_CUMULUS_VDDCORE	24
1E67	VOLTAGE=1.2V	PP_CUMULUS_VDDANA	24
1E68	VOLTAGE=13.5V	PP_SAGE_TO_TOUCH_VCPH_CONN	24
1E69	VOLTAGE=-12V	PP_SAGE_TO_TOUCH_VCPH_CONN	24
1E6A	VOLTAGE=13.5V	PP_SAGE_TO_TOUCH_VCPH	24
1E6B	VOLTAGE=-12V	PP_SAGE_TO_TOUCH_VCPH	24
1E6C	VOLTAGE=-12V	PP_SAGE_VCPH_F	24
1E6D	VOLTAGE=5.7V	PP_SAGE_LX	24
1E6E	VOLTAGE=17.0V	PP_SAGE_LX1	24
1E6F	VOLTAGE=1.8V	PP_PMIJ_VREF	13
1E70	VOLTAGE=14V	PP_SAGE_VBST_OUTH	24
1E71	VOLTAGE=5.0V	PP_TTGRIS_VBUS_DET	14
1E72	VOLTAGE=2.5V	PP_PMIJ_VDD_REF	13
1E73	VOLTAGE=1.8V	PP_EXTMTC_BIAS	10
1E74	VOLTAGE=1.8V	PP1V8_XTAL	2
1E75	VOLTAGE=1.8V	PP_PMIJ_VDD_RTC	13
1E76	VOLTAGE=4.6V	PP_BATT_VCC	14 16 25 40 45 46
1E77	VOLTAGE=1.8V	PP1V8_MESA_CONN	21
1E78	VOLTAGE=3.0V	PP3V0_PROX_CONN	11
1E79	VOLTAGE=1.0V	PP0V95_FIXED_SOC	4 7 12
1E7A	VOLTAGE=1.0V	PP0V95_FIXED_SOC_PCIE	7
1E7B	VOLTAGE=1.2V	PP1V2_PL1	2
1E7C	VOLTAGE=1.0V	PP_BUCK5_LX1	12
1E7D	VOLTAGE=1.0V	PP_VAR_SOC	5 12
1E7E	VOLTAGE=5.0V	PMID_CAP	14
1E7F	VOLTAGE=5.0V	CHARGER_I1DO	14
1E79	VOLTAGE=4.6V	CHG_BOOT	14
1E7A	VOLTAGE=4.6V	CHG_LX	14
1E7B	VOLTAGE=3.0V	VTRF_DRIVE_P	14 18
1E7C	VOLTAGE=3.0V	VTRF_DRIVE_N	14 18
1E7D	VOLTAGE=1.8V	PP_RCAM_AE_CONN	23
1E7E	VOLTAGE=-14.0V	PP_SAGE_VBST_OUTL	24
1E7F	VOLTAGE=-12.0V	PP_SAGE_TO_TOUCH_VCPH_FTILT	24
1E79	VOLTAGE=2.7V	PP_BB_VDD_2V7_CONN	18

PAGE TITLE		SYSTEM:VOLTAGE PROPERTIES	
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N61 SPECIFIC

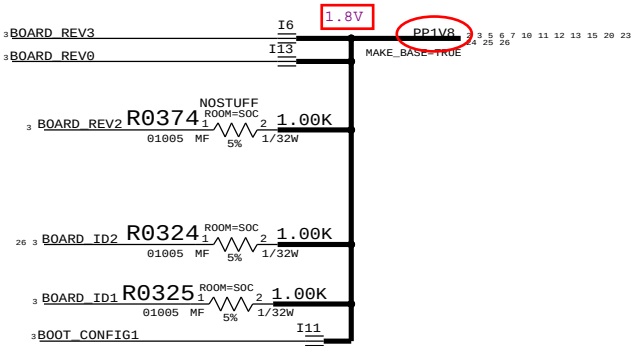
BOOTSTRAPPING (BOARD_REV, BOARD_ID, BOOT_CFG)



BOARD_REV[3:0]={GPIO34, GPIO35, GPIO36, GPIO37}
FLOAT=LOW, PULLUP=HIGH
1111 PROTOMLB1
1110 PROTOMLB2
1101 PROTO1
1100 PROTO2
1011 EVT
1010 EVT SPLIT CARBON DOE
1001 CARRIER BUILD <--- SELECTED
1000 DVT

BOARD_ID[4:0]={GPIO29, GPIO16, SPI00_MISO, SPI0_MOSI, SPI0_SCLK}
FLOAT=LOW, PULLUP=HIGH
00100 N56, T133 MLB
00101 N56 DEV
00110 FIJI N61 MLB <--- SELECTED

BOOT_CONFIG[2:0]={GPIO28, GPIO25, GPIO18}
FLOAT=LOW, PULLUP=HIGH
000 SPI0
001 SPI0 TEST MODE
010 NAND <--- SELECTED
011 NAND TEST MODE
100 NVME
101 NVME TEST MODE
111 FAST SPI



SYSTEM:N61 SPECIFIC

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PAGE
27 OF 55

SHEET
27 OF 54

RADIO_MLB HIERARCHICAL SYMBOL

POWER

VCC_MAIN, VBAT GOES TO RADIO_MLB DIRECTLY
CHECK ALL PAGES IN RF SIDE!

CELLULAR HOUSE KEEPING

3	AP_TO_RADIO_ON_L	MAKE_BASE=TRUE	I325	RADIO_ON_L	30 32
3	BB_TO_AP_RESET_DET_L	MAKE_BASE=TRUE	I324	BB_RESET_DET_L	30 31
13	PMU_TO_BB_RST_L	MAKE_BASE=TRUE	I326	RF_PMIC_RESET_L	30 32
3	AP_TO_BB_RST_L	MAKE_BASE=TRUE	I327	BB_RST_L	30 32
3	AP_TO_BB_WAKE_MODEM	MAKE_BASE=TRUE	I329	AP_WAKE_MODEM	35
13	BB_TO_PMU_HOST_WAKE_L	MAKE_BASE=TRUE	I328	BB_WAKE_HOST_L	30 35
3	BB_TO_AP_IPC_GPIO	MAKE_BASE=TRUE	I331	BB_IPC_GPIO	35
16	BB_TO_LEDDRVS_GSM_BLANK	MAKE_BASE=TRUE	I330	GSM_TXBURST_IND	35
3	BB_TO_AP_GPS_SYNC	MAKE_BASE=TRUE	I332	BB_GPS_SYNC	30 35

HSIC IPC

2	50_AP_BT_BB_HSIC1_DATA	MAKE_BASE=TRUE	I368	50_BB_HSIC_DATA	30 34
2	50_AP_BT_BB_HSIC1_STB	MAKE_BASE=TRUE	I369	50_BB_HSIC_STROBE	30 34
3	AP_TO_BB_HOST_RDY	MAKE_BASE=TRUE	I371	BB_HOST_RDY	30 35
3	BB_TO_AP_DEVICE_RDY	MAKE_BASE=TRUE	I370	BB_DEVICE_RDY	30 35
3	BB_TO_AP_IPC_GPIO1	MAKE_BASE=TRUE	I372	BB_IPC_GPIO1	35

UART IPC

3	AP_TO_BB_UART2_RTS_L	MAKE_BASE=TRUE	I373	BB_UART_CTS_L	30 35
3	BB_TO_AP_UART2_CTS_L	MAKE_BASE=TRUE	I376	BB_UART_RTS_L	30 35
17 3	AP_TO_BB_UART2_TXD	MAKE_BASE=TRUE	I374	BB_UART_RXD	30 35
17 3	BB_TO_AP_UART2_RXD	MAKE_BASE=TRUE	I375	BB_UART_TXD	30 35

AUDIO I2S

3	45_AP_TO_BB_I2S3_BCLK	MAKE_BASE=TRUE	I377	BB_I2S_CLK	35
3	AP_TO_BB_I2S3_DOUT	MAKE_BASE=TRUE	I378	BB_I2S_RXD	30 35
3	BB_TO_AP_I2S3_DIN	MAKE_BASE=TRUE	I379	BB_I2S_TXD	30 35
3	AP_TO_BB_I2S3_LRCLK	MAKE_BASE=TRUE	I380	BB_I2S_WS	35

OSCAR UART

22	OSCAR_TO_BB_UART_TXD	MAKE_BASE=TRUE	I382	BB_OTHER_RXD	30 35
22	BB_TO_OSCAR_UART_RXD	MAKE_BASE=TRUE	I381	BB_OTHER_TXD	30 35

BB DEBUG INTERFACES

3	AP_TO_BB_COREDUMP	MAKE_BASE=TRUE	I384	BB_CORE_DUMP	30 35
13	PMU_TO_BB_VBUS_DET	MAKE_BASE=TRUE	I387	BB_USB_VBUS	30 34
17	90_TRISTAR_BT_BB_USB_N	MAKE_BASE=TRUE	I386	90_BB_USB_N	30 34
17	90_TRISTAR_BT_BB_USB_P	MAKE_BASE=TRUE	I388	90_BB_USB_P	30 34

RADIO ANTENNA CONTROL

18	PP_BB_VDD_2V7	MAKE_BASE=TRUE	I389	PP_LD014_RFSW	31 41 42
18	BB_GPIO0	MAKE_BASE=TRUE	I390	BB_LAT_GPIO0	35
18	BB_GPIO2	MAKE_BASE=TRUE	I391	BB_LAT_GPIO2	35
18	BB_GPIO3	MAKE_BASE=TRUE	I392	BB_LAT_GPIO3	35
18	BB_GPIO4	MAKE_BASE=TRUE	I394	BB_LAT_GPIO4	35

FCT TESTING

13	RADIO_TO_PMU_ADC_SMPS1	MAKE_BASE=TRUE	I395	ADC_SMPS1	30
13	RADIO_TO_PMU_ADC_PP_LD011_VDDIO	MAKE_BASE=TRUE	I396	ADC_PP_LD011	30
13	RADIO_TO_PMU_ADC_PP_LD05_SIM	MAKE_BASE=TRUE	I398	ADC_PP_LD05	30
13	RADIO_TO_PMU_ADC_SMPS4	MAKE_BASE=TRUE	I397	ADC_SMPS4	30

UPPER RADIO ANTENNA CONTROL

25	50_AP_WIFI_5G_CONN_ANT	MAKE_BASE=TRUE	I410	50_WIFI_5G_CONN_ANT	50
25	50_AP_UAT_FEED	MAKE_BASE=TRUE	I409	50_UPPER_ANT_FEED	50
13	UAT_ANT_GND	MAKE_BASE=TRUE	I411	ANT_GND	50
20 26 17 15 12	PP3V0_TRISTAR	MAKE_BASE=TRUE	I404	PAC_VDD_3V0	53
25 8	NORTH_AC_GND_SCREW	MAKE_BASE=TRUE	I412	NORTH_ANT_GND	50

POWER

26 17 15 14 13 12 10 4 3	PP1V8_SDRAM	MAKE_BASE=TRUE	I314	PP_WL_BT_VDDIO_AP	51
		MAKE_BASE=TRUE	I315	PP_STOCKHOLM_1V8_S2R	52 54
		MAKE_BASE=TRUE	I407	REFE_VT0_S2R	53

WLAN/BT HOUSE KEEPING

13	45_PMU_TO_WLAN_CLK32K	MAKE_BASE=TRUE	I316	CLK32K_AP	30 51
13	PMU_TO_WLAN_REG_ON	MAKE_BASE=TRUE	I317	WLAN_REG_ON	30 51
13	WLAN_TO_PMU_HOST_WAKE	MAKE_BASE=TRUE	I318	HOST_WAKE_WLAN	30 51
13	PMU_TO_BT_REG_ON	MAKE_BASE=TRUE	I319	BT_REG_ON	30 51
3	AP_TO_BT_WAKE	MAKE_BASE=TRUE	I320	WAKE_BT	30 51
13	BT_TO_PMU_HOST_WAKE	MAKE_BASE=TRUE	I321	HOST_WAKE_BT	51

3	AP_TO_WLAN_JTAG_SWCLK	MAKE_BASE=TRUE	I333	WLAN_JTAG_SWCLK	30 51
3	AP_TO_WLAN_JTAG_SWDIO	MAKE_BASE=TRUE	I334	WLAN_JTAG_SWDIO	30 51
13	WLAN_TO_PMU_PCIE_WAKE_L	MAKE_BASE=TRUE	I335	WLAN_PCIE_WAKE_L	30 51
3	AP_TO_WLAN_DEVICE_WAKE	MAKE_BASE=TRUE	I336	PCIE_DEV_WAKE	30 51
7	90_WLAN_TO_AP_PCIE1_RXDP_P	MAKE_BASE=TRUE	I337	90_WLAN_PCIE_TDP	30 51
7	90_WLAN_TO_AP_PCIE1_RXDP_N	MAKE_BASE=TRUE	I340	90_WLAN_PCIE_TDN	30 51
7	90_AP_TO_WLAN_PCIE1_TXDP_P	MAKE_BASE=TRUE	I338	90_WLAN_PCIE_RDP	30 51
7	90_AP_TO_WLAN_PCIE1_TXDP_N	MAKE_BASE=TRUE	I339	90_WLAN_PCIE_RDN	30 51
7	90_AP_TO_WLAN_PCIE1_REFCLK1_P	MAKE_BASE=TRUE	I342	90_WLAN_PCIE_REFCLK_P	51
7	90_AP_TO_WLAN_PCIE1_REFCLK1_N	MAKE_BASE=TRUE	I341	90_WLAN_PCIE_REFCLK_N	51
7	WLAN_TO_AP_PCIE1_CLKREQ_L	MAKE_BASE=TRUE	I344	WLAN_PCIE_CLKREQ_L	30 51
7	AP_TO_WLAN_PCIE1_RST_L	MAKE_BASE=TRUE	I343	WLAN_PCIE_PERST_L	30 51

WLAN HSIC IPC

3	WLAN_TO_AP_UART4_RXD	MAKE_BASE=TRUE	I345	WLAN_UART_TXD	30 51
3	AP_TO_WLAN_UART4_TXD	MAKE_BASE=TRUE	I348	WLAN_UART_RXD	30 51
3	WLAN_TO_AP_UART4_CTS_L	MAKE_BASE=TRUE	I347	WLAN_UART_RTS_L	30 51
3	AP_TO_WLAN_UART4_RTS_L	MAKE_BASE=TRUE	I346	WLAN_UART_CTS_L	30 51

BT UART IPC

3	AP_TO_BT_UART1_RTS_L	MAKE_BASE=TRUE	I349	BT_UART_CTS_L	51
3	BT_TO_AP_UART1_CTS_L	MAKE_BASE=TRUE	I352	BT_UART_RTS_L	51
3	AP_TO_BT_UART1_TXD	MAKE_BASE=TRUE	I351	BT_UART_RXD	30 51
3	BT_TO_AP_UART1_RXD	MAKE_BASE=TRUE	I350	BT_UART_TXD	30 51

BT AUDIO PCM

3	45_AP_TO_BT_I2S1_BCLK	MAKE_BASE=TRUE	I354	BT_PCM_CLK	51
3	AP_TO_BT_I2S1_DOUT	MAKE_BASE=TRUE	I353	BT_PCM_IN	51
3	BT_TO_AP_I2S1_DIN	MAKE_BASE=TRUE	I355	BT_PCM_OUT	51
3	AP_TO_BT_I2S1_LRCLK	MAKE_BASE=TRUE	I356	BT_PCM_SYNC	51

OSCAR STATES

22	OSCAR_TO_RADIO_CONTEXT_A	MAKE_BASE=TRUE	I358	OSCAR_CONTEXT_A	51
22	OSCAR_TO_RADIO_CONTEXT_B	MAKE_BASE=TRUE	I357	OSCAR_CONTEXT_B	51

STOCKHOLM

3	STOCKHOLM_TO_AP_UART3_CTS_L	MAKE_BASE=TRUE	I359	STOCKHOLM_RTS_L	30 52
3	AP_TO_STOCKHOLM_UART3_RTS_L	MAKE_BASE=TRUE	I360	STOCKHOLM_CTS_L	30 52
3	STOCKHOLM_TO_AP_UART3_RXD	MAKE_BASE=TRUE	I361	STOCKHOLM_UART_TXD	30 52
3	AP_TO_STOCKHOLM_UART3_TXD	MAKE_BASE=TRUE	I363	STOCKHOLM_UART_RXD	30 52
3	AP_TO_STOCKHOLM_DWLD_REQ	MAKE_BASE=TRUE	I362	STOCKHOLM_FW_DWLD_REQ	52
13	STOCKHOLM_TO_PMU_HOST_WAKE	MAKE_BASE=TRUE	I364	STOCKHOLM_HOST_WAKE	30 52
7	AP_TO_STOCKHOLM_EN	MAKE_BASE=TRUE	I365	STOCKHOLM_ENABLE	52
20 26 17 15 12	PP3V0_TRISTAR	MAKE_BASE=TRUE	I366	STOCKHOLM_VDD_MUX_3V0	54
3	AP_TO_STOCKHOLM_SIM_SEL	MAKE_BASE=TRUE	I367	STOCKHOLM_SIM_SEL	54
25	AP_TO_STOCKHOLM_ANT	MAKE_BASE=TRUE	I406	STOCKHOLM_ANT	52

CELL:ALIASES



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PAGE 30 OF 55

SHEET 29 OF 54

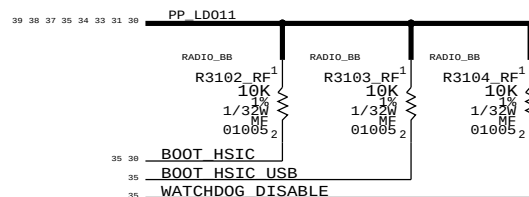
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AP INTERFACE & DEBUG CONNECTORS

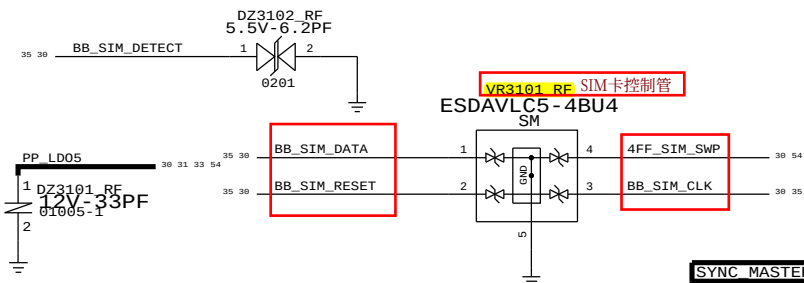
PROBE POINTS

PP3105 RF P2MM-NSM SM 1 PP 1 CLK32K_AP WIFI_BT 29 51	PP3121 RF P2MM-NSM SM 1 STOCKHOLM_HOST_WAKE RADIO_STOCKHOLM 29 52	PP3115 RF P2MM-NSM SM 1 50_BB_HSIC_STROBE SIM_DEBUG 29 34	PP3130 RF P2MM-NSM SM 1 BB_JTAG_RST_L SIM_DEBUG 34	PP3141 RF P2MM-NSM SM 1 BB_UART_TXD SIM_DEBUG 29 35	PP3170 RF P2MM-NSM SM 1 RFFE1_CLK RF_DEBUG 35 39 40 41 42 43 44
PP3113 RF P2MM-NSM SM 1 BB_COEX_UART_RXD WIFI_BT 35 51	PP3122 RF P2MM-NSM SM 1 BB_REQUEST_X0_CLK SIM_DEBUG 32 52	PP3116 RF P2MM-NSM SM 1 50_BB_HSIC_DATA SIM_DEBUG 29 34	PP3131 RF P2MM-NSM SM 1 BB_JTAG_TCK SIM_DEBUG 34	PP3142 RF P2MM-NSM SM 1 BB_UART_RXD SIM_DEBUG 29 35	PP3171 RF P2MM-NSM SM 1 RFFE1_DATA RF_DEBUG 35 39 40 41 42 43 44
PP3114 RF P2MM-NSM SM 1 BB_COEX_UART_TXD WIFI_BT 35 51	PP3123 RF P2MM-NSM SM 1 STOCKHOLM_UART_RXD RADIO_STOCKHOLM 29 52	PP3101 RF P2MM-NSM SM 1 BB_DEBUG_ERROR SIM_DEBUG 35	PP3132 RF P2MM-NSM SM 1 BB_JTAG_TMS SIM_DEBUG 34	PP3143 RF P2MM-NSM SM 1 BB_UART_RTS_L SIM_DEBUG 29 35	PP3172 RF P2MM-NSM SM 1 RFFE2_CLK RF_DEBUG 35 45 46 48
PP3119 RF P2MM-NSM SM 1 BT_UART_TXD WIFI_BT 29 51	PP3124 RF P2MM-NSM SM 1 STOCKHOLM_UART_TXD RADIO_STOCKHOLM 29 52	PP3102 RF P2MM-NSM SM 1 RF_PMIC_RESET_L SIM_DEBUG 29 32	PP3133 RF P2MM-NSM SM 1 BB_JTAG_TDO SIM_DEBUG 34	PP3144 RF P2MM-NSM SM 1 BB_UART_CTS_L SIM_DEBUG 29 35	PP3173 RF P2MM-NSM SM 1 RFFE2_DATA RF_DEBUG 35 45 46 48
PP3120 RF P2MM-NSM SM 1 BT_UART_RXD WIFI_BT 29 51	PP3125 RF P2MM-NSM SM 1 STOCKHOLM_CTS_L RADIO_STOCKHOLM 29 52	PP3103 RF P2MM-NSM SM 1 PS_HOLD_PMIC SIM_DEBUG 32	PP3134 RF P2MM-NSM SM 1 BB_JTAG_TDI SIM_DEBUG 34	PP3145 RF P2MM-NSM SM 1 BB_HOST_RDY SIM_DEBUG 29 35	PP3175 RF P2MM-NSM SM 1 BB_I2S_WS RF_DEBUG 29 35
PP3152 RF P2MM-NSM SM 1 WAKE_BT WIFI_BT 29 51	PP3126 RF P2MM-NSM SM 1 STOCKHOLM_RTS_L RADIO_STOCKHOLM 29 52	PP3127 RF P2MM-NSM SM 1 PMIC_RESOUT_L SIM_DEBUG 32 34	PP3135 RF P2MM-NSM SM 1 BB_JTAG_TRST_L SIM_DEBUG 34	PP3146 RF P2MM-NSM SM 1 BB_DEVICE_RDY SIM_DEBUG 29 35	PP3176 RF P2MM-NSM SM 1 BB_I2S_RXD RF_DEBUG 29 35
PP3153 RF P2MM-NSM SM 1 WLAN_REG_ON WIFI_BT 29 51	PP3128 RF P2MM-NSM SM 1 PP_PN65_VCC_SIM SIM_DEBUG 52	PP3104 RF P2MM-NSM SM 1 MDM_CLK SIM_DEBUG 32 34	PP3136 RF P2MM-NSM SM 1 BB_DEBUG_STATUS SIM_DEBUG 35	PP3147 RF P2MM-NSM SM 1 BB_GPS_SYNC SIM_DEBUG 29 35	PP3177 RF P2MM-NSM SM 1 BB_I2S_TXD RF_DEBUG 29 35
PP3154 RF P2MM-NSM SM 1 BT_REG_ON WIFI_BT 29 51	PP3174 RF P2MM-NSM SM 1 STOCKHOLM_SIM_SWP SIM_DEBUG 52 54	PP3109 RF P2MM-NSM SM 1 PP_LD011 SIM_DEBUG 38 39 33 34 35 37 38	PP3137 RF P2MM-NSM SM 1 BB_CORE_DUMP SIM_DEBUG 29 35	PP3148 RF P2MM-NSM SM 1 BB_WAKE_HOST_L SIM_DEBUG 29 35	PP3178 RF P2MM-NSM SM 1 BB_OTHER_TXD RF_DEBUG 29 35
PP3155 RF P2MM-NSM SM 1 HOST_WAKE_WLAN WIFI_BT 29 51	PP3129 RF P2MM-NSM SM 1 REF_CLK_FROM_BB SIM_DEBUG 32 52	PP3110 RF P2MM-NSM SM 1 RADIO_ON_L SIM_DEBUG 29 32	PP3138 RF P2MM-NSM SM 1 BB_USB_VBUS SIM_DEBUG 29 34	PP3149 RF P2MM-NSM SM 1 BB_RESET_DET_L SIM_DEBUG 29 35	PP3179 RF P2MM-NSM SM 1 BB_OTHER_RXD RF_DEBUG 29 35
PP3156 RF P2MM-NSM SM 1 WLAN_PCIE_WAKE_L WIFI_BT 29 51	PP3165 RF P2MM-NSM SM 1 DSDS_SIM_CLK SIM_DEBUG 34 54	PP3127 RF P2MM-NSM SM 1 SPMI_DATA SIM_DEBUG 32 34	PP3139 RF P2MM-NSM SM 1 90_BB_USB_N SIM_DEBUG 29 34	PP3150 RF P2MM-NSM SM 1 BB_RST_L SIM_DEBUG 29 32	
PP3157 RF P2MM-NSM SM 1 WLAN_PCIE_PERST_L WIFI_BT 29 51	PP3183 RF P2MM-NSM SM 1 DSDS_SIM_RESET SIM_DEBUG 34 54	PP3112 RF P2MM-NSM SM 1 SPMI_CLK SIM_DEBUG 32 34	PP3140 RF P2MM-NSM SM 1 90_BB_USB_P SIM_DEBUG 29 34		
PP3158 RF P2MM-NSM SM 1 WLAN_PCIE_CLKREQ_L WIFI_BT 29 51	PP3184 RF P2MM-NSM SM 1 DSDS_SIM_DATA SIM_DEBUG 34 54				
PP3159 RF P2MM-NSM SM 1 PCIE_DEV_WAKE WIFI_BT 29 51	PP3186 RF P2MM-NSM SM 1 DSDS_SIM_DETECT SIM_DEBUG 34				
PP3160 RF P2MM-NSM SM 1 WLAN_UART_RTS_L WIFI_BT 29 51	PP3187 RF P2MM-NSM SM 1 PP_LD06 SIM_DEBUG 31 33 54				
PP3161 RF P2MM-NSM SM 1 WLAN_UART_CTS_L WIFI_BT 29 51	PP3188 RF P2MM-NSM SM 1 DSDS_SIM_SWP SIM_DEBUG 54				
PP3162 RF P2MM-NSM SM 1 WLAN_UART_RXD WIFI_BT 29 51	PP3189 RF P2MM-NSM SM 1 DSDS_SIM_DATA_R SIM_DEBUG 54				
PP3163 RF P2MM-NSM SM 1 WLAN_UART_TXD WIFI_BT 29 51					
PP3190 RF P2MM-NSM SM 1 WLAN_JTAG_SWDCCLK WIFI_BT 29 51	PP 3178 RF P2MM-NSM SM 1 BB_SIM_RESET SIM_DEBUG 30 35				
PP3191 RF P2MM-NSM SM 1 WLAN_JTAG_SWDIO WIFI_BT 29 51	PP 3179 RF P2MM-NSM SM 1 BB_SIM_CLK SIM_DEBUG 30 35				
	PP 3180 RF P2MM-NSM SM 1 BB_SIM_DATA SIM_DEBUG 30 35				
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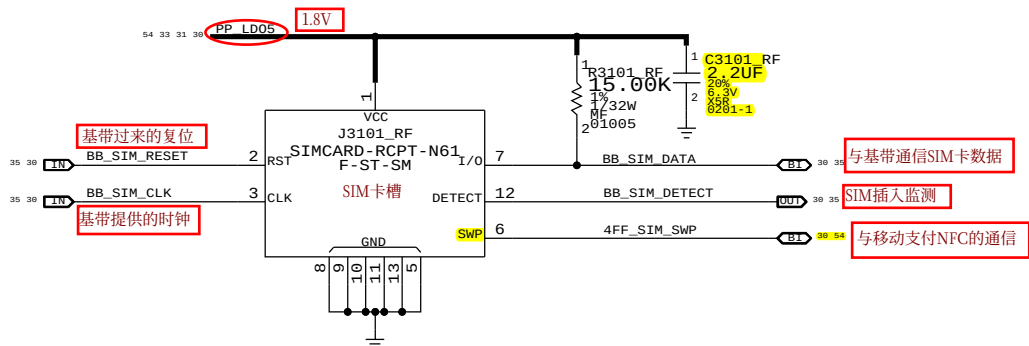
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197S0598	197S0593	ALTERNATE	Y3301_RF	AVX 19.2MHZ XTAL
138S00005	138S00003	ALTERNATE	C3216_RF	150F CAPACITOR
138S0739	138S0706	ALTERNATE	C4207_RF	1.0UF CAPACITOR
138S0945	138S0706	ALTERNATE	C4207_RF	1.0UF CAPACITOR
138S1103	138S0719	ALTERNATE	C4007_RF	4.7UF CAPACITOR
339S0231	339S0228	ALTERNATE	U5201_RF	CORONA MODULE USI
339S0242	339S0228	ALTERNATE	U5201_RF	CORONA MODULE TDK
155S00024	155S0950	ALTERNATE	F_TRI_RF	TRIPLEXER BIN2



SIM CARD ESD PROTECTION



SIM CARD CONNECTOR



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基带电源

SWITCHERS OUTPUT CAPS



SWITCHERS BULK CAPS



此电感进水腐蚀导致无基带

BASEBAND PMU (1 OF 2)



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C401
R411
L400
U404

D

C

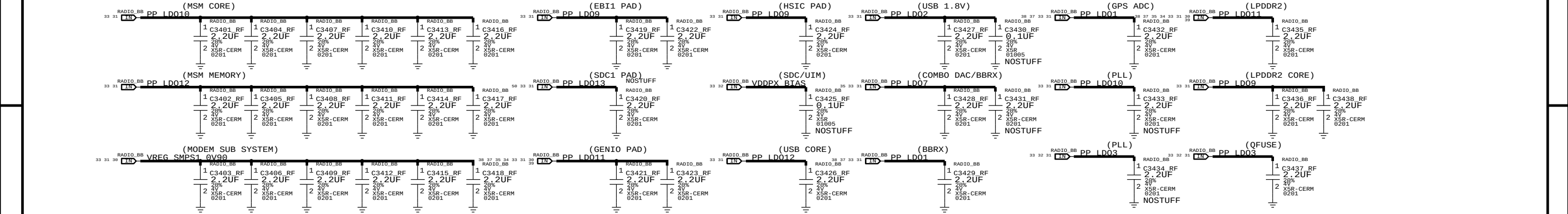
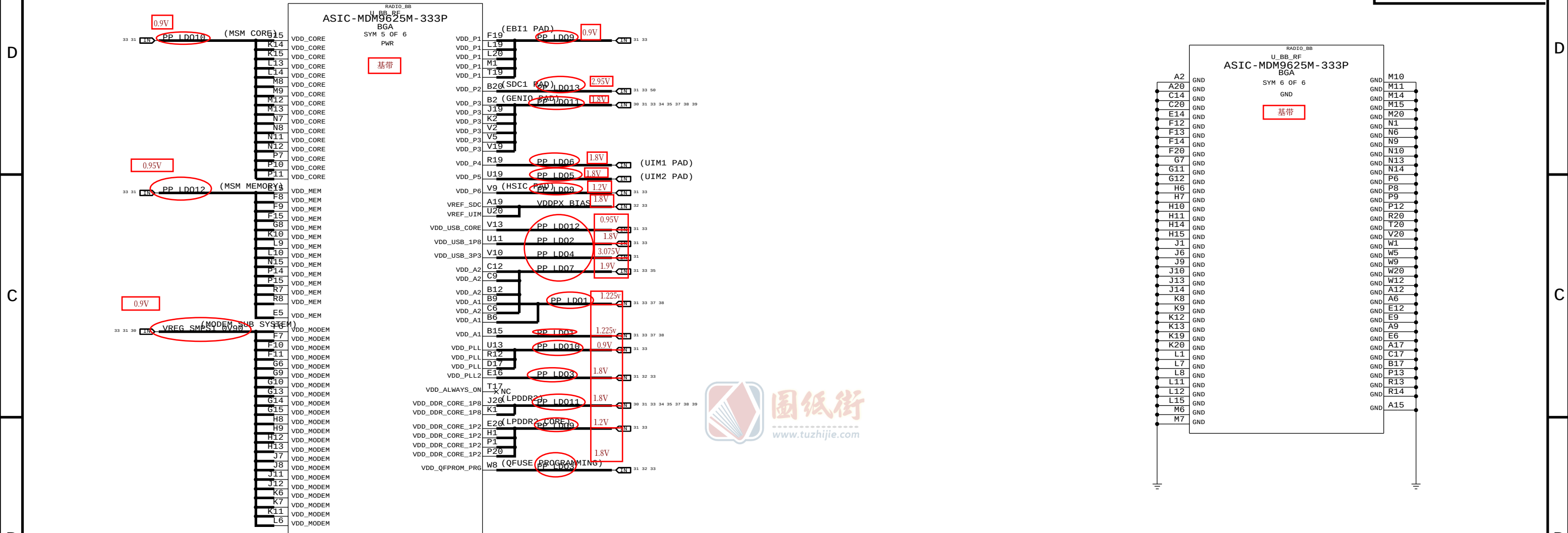
B

A

BASEBAND (1 OF 3)

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C538
R500
L500
U502



BASEBAND (1 OF 2)

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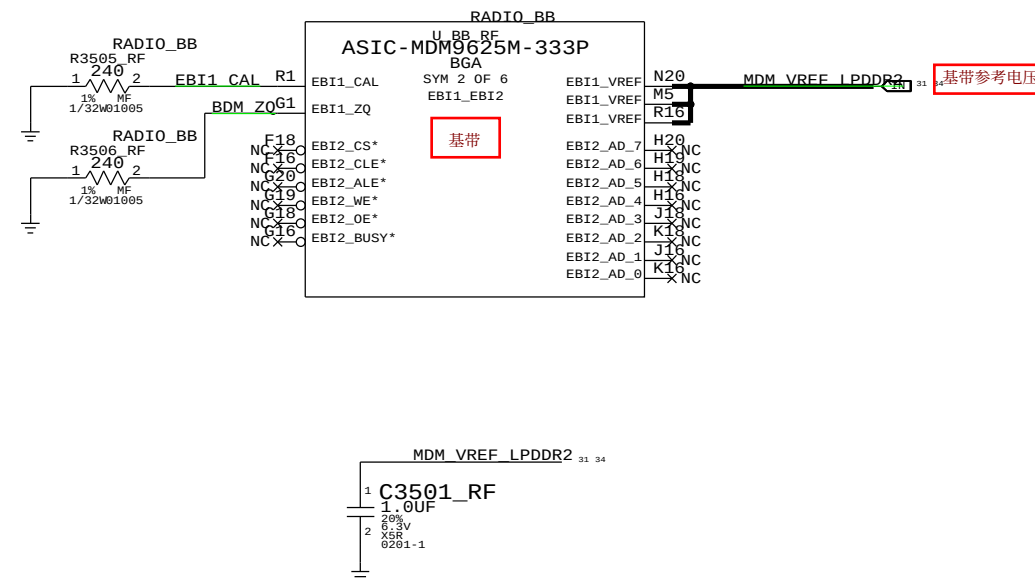
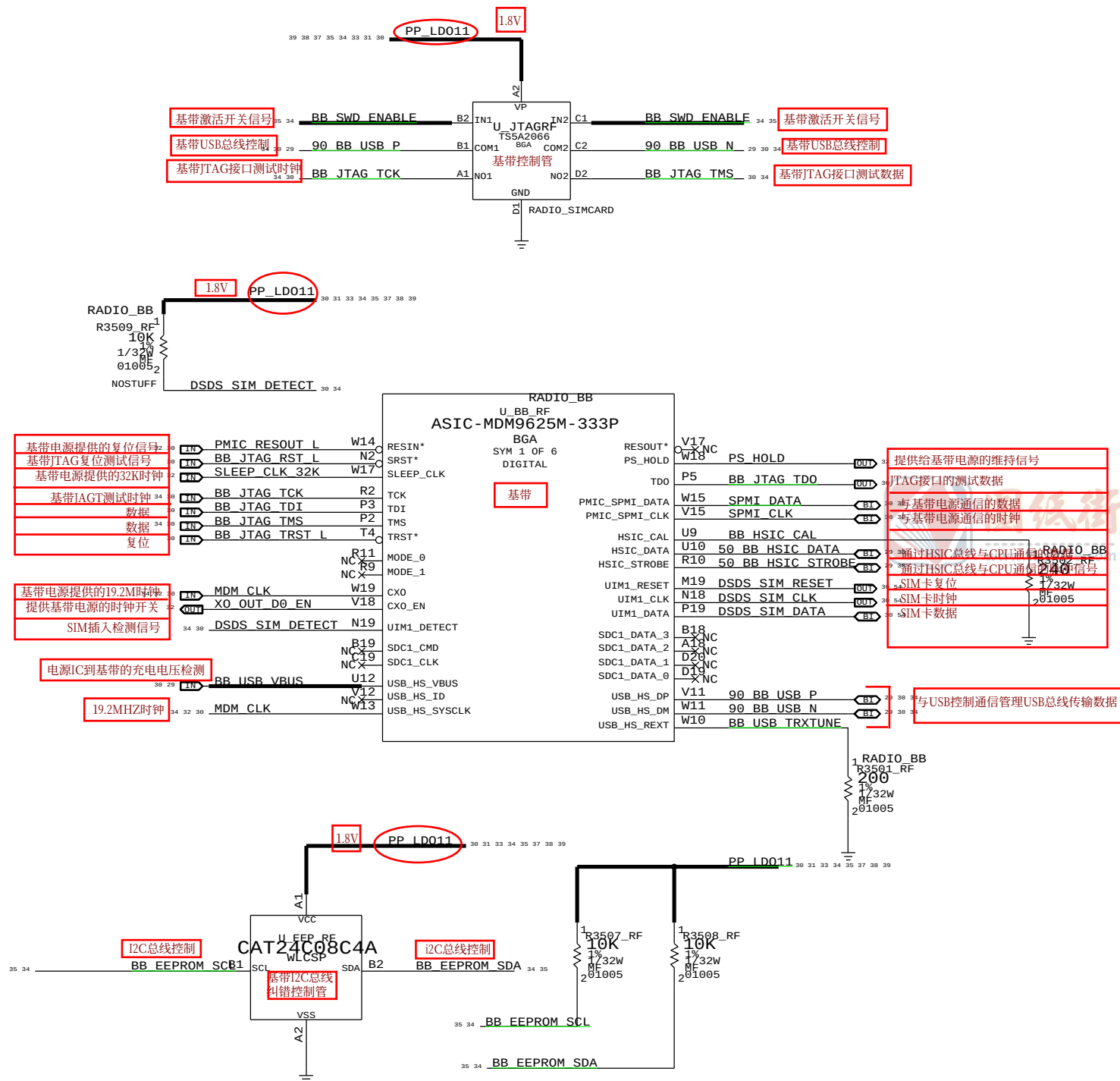
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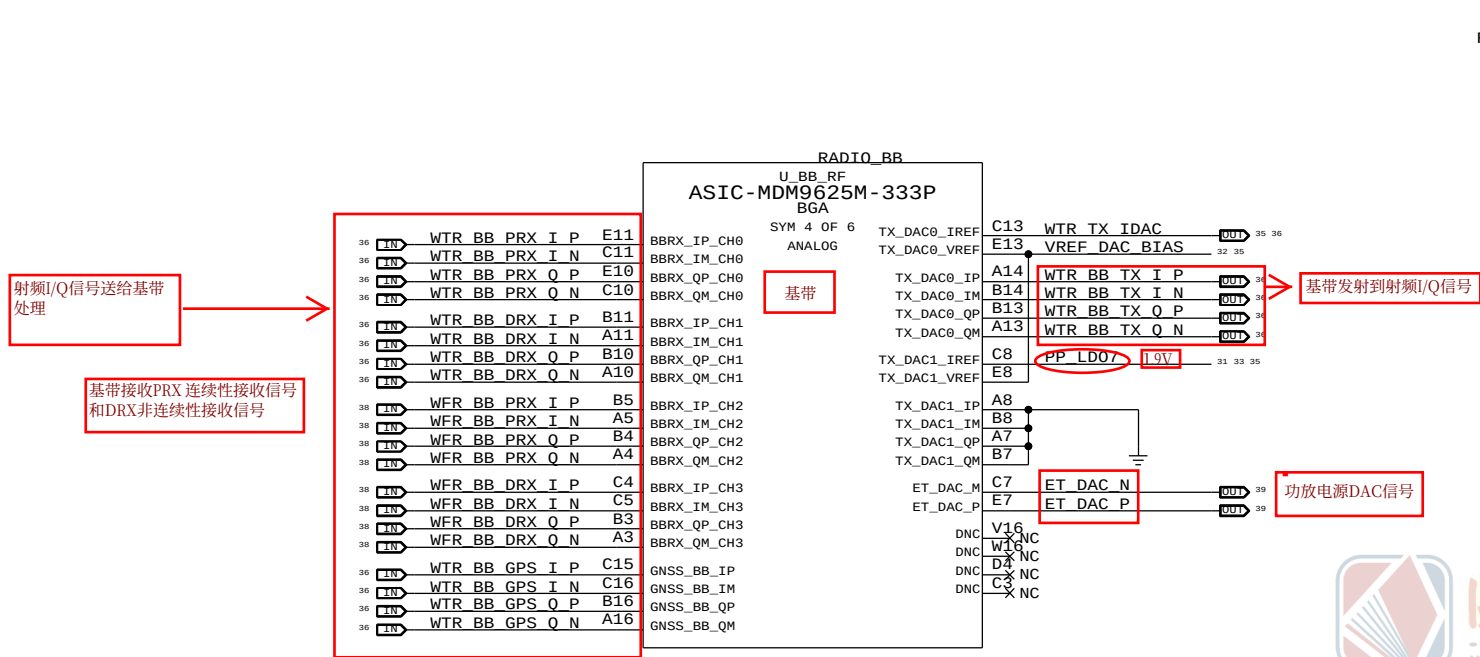
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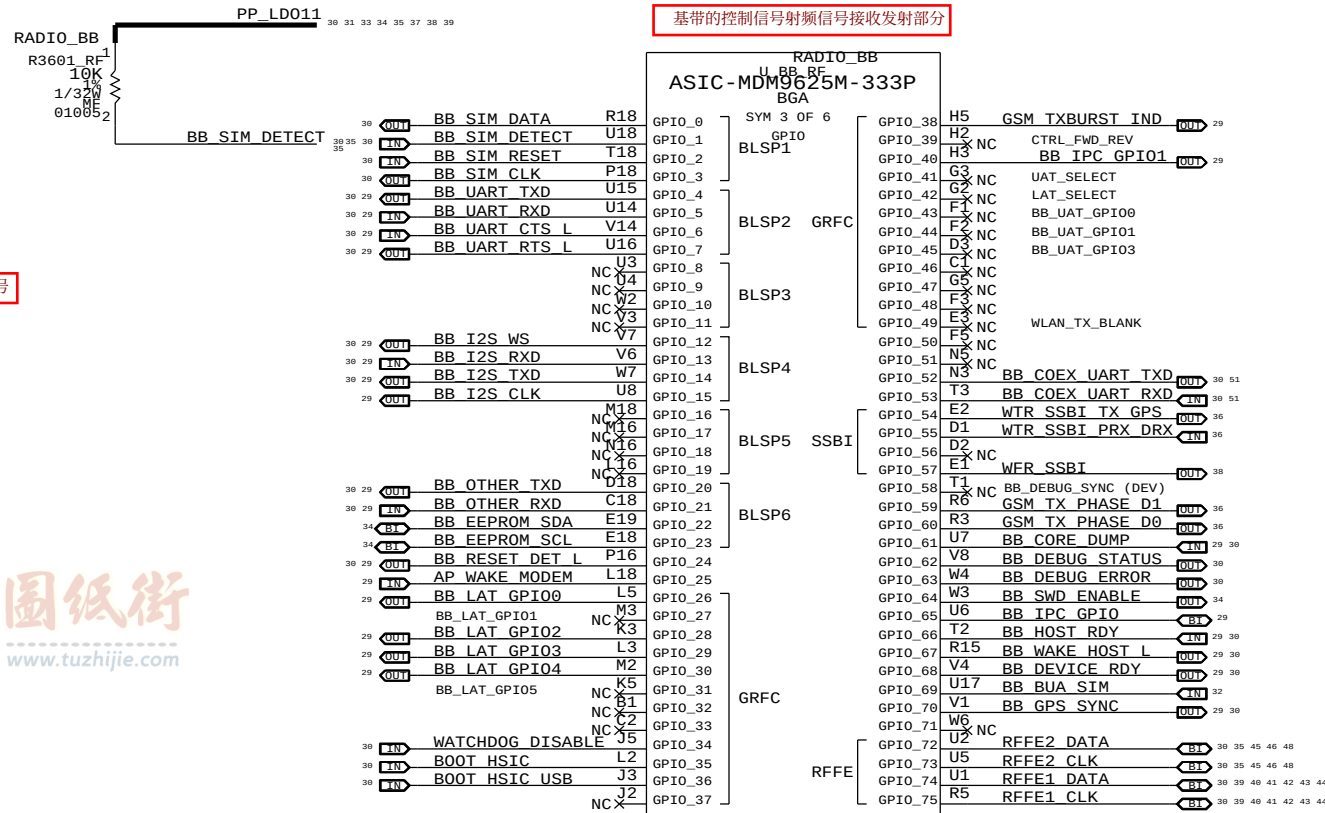
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C704
R700
L700
U702



射频I/Q信号送给基带处理

基带接收PRX连续性接收信号和DRX非连续性接收信号



基带的控制信号射频信号接收发射部分

基带时钟数据控制管

1.8V

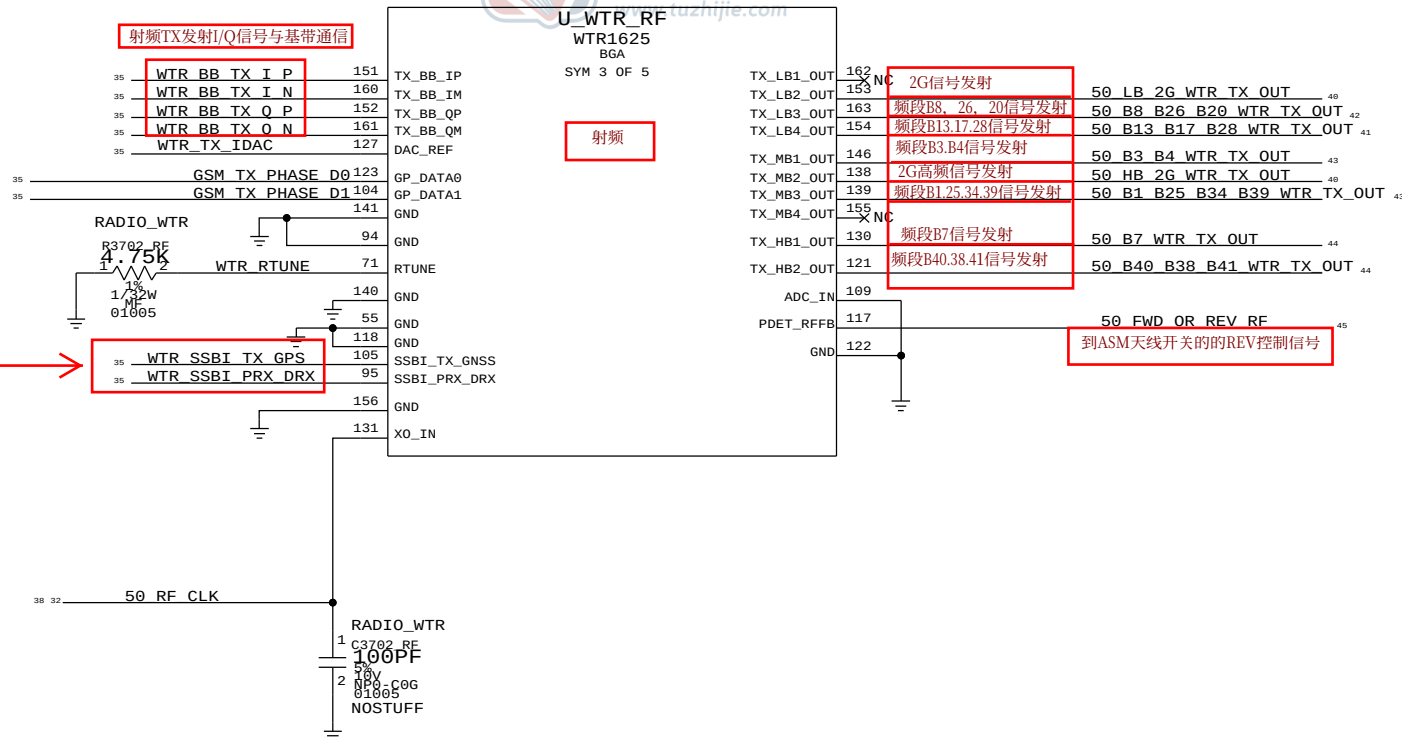
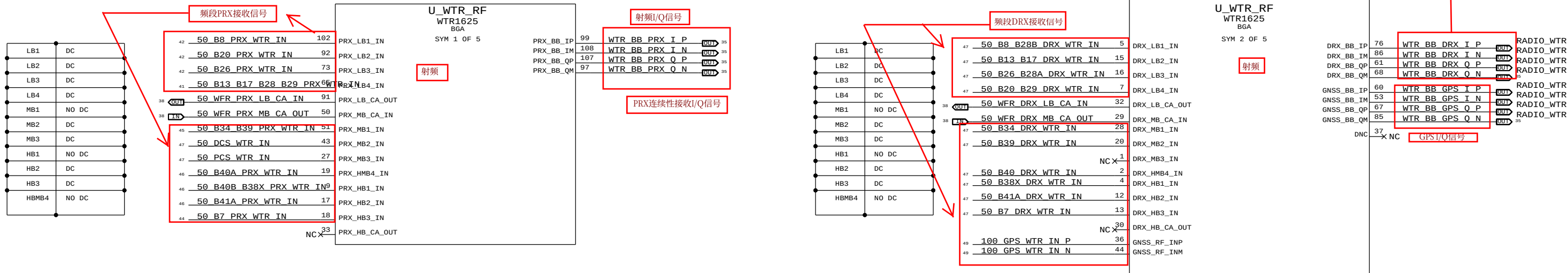
MOBILE DATA MODEM (2 OF 2)

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WTR TRANSCEIVER (1 OF 2)

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C802
R802
L800
U803



RF TRANSCEIVER (1 OF 3)

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WTR TRANSCEIVER (2 OF 2)

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WTR DECOUPLING CAPS

L3801 RF
22NH-3%-0.25A

1 0201 RADIO_WTR

2 VDD_PRX_PLL 1P3V 37

VREG 1P3V

MAKE_BASE=TRUE

VDD_PRX_LO_HB 1P3V 37

RADIO_WTR

1 C3829 RE

2 10UF

20% 3V CERM-XSR 0402

VDD_TX_DA 2V 37

RADIO_WTR

1 C3812 RE

2 100PF

20% 5V NPO-C0G 01005

VDD_TX_BB 2V 37

RADIO_WTR

1 C3813 RE

2 100PF

20% 5V NPO-C0G 01005

VDD_TX_BBF 2V 37

RADIO_WTR

1 C3814 RE

2 100PF

20% 5V NPO-C0G 01005

VDD_PRX_VCO 2V 37

RADIO_WTR

1 C3815 RE

2 10UF

20% 3V XSR 01005

VDD_SHDR_VCO 1P3V 37

RADIO_WTR

1 C3823 RE

2 10UF

20% 3V XSR 01005

VDD_PRX_VCO 1P3V 37

RADIO_WTR

1 C3824 RE

2 10UF

20% 3V XSR 01005

VDD_TX_VCO 2V 37

RADIO_WTR

1 C3817 RE

2 10UF

20% 3V XSR 01005

VDD_TX_PLL 2V 37

RADIO_WTR

1 C3802 RE

2 10UF

20% 3V XSR 01005

VDD_TX_SYNTH 1P3V 37

RADIO_WTR

1 C3824 RE

2 10UF

20% 3V XSR 01005

VDD_TX_LO 1P3V 37

RADIO_WTR

1 C3825 RE

2 10UF

20% 3V XSR 01005

VDD_TX_UPC 1P3V 37

RADIO_WTR

1 C3826 RE

2 100PF

20% 5V NPO-C0G 01005

VDD_TX_VCO 1P3V 37

RADIO_WTR

1 C3819 RE

2 10UF

20% 3V XSR 01005

VDD_TX_VCO 1P3V 37

RADIO_WTR

1 C3827 RE

2 100PF

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VDD_TX_VCO 1P3V 37

RADIO_WTR

1 C3827 RE

2 100PF

20% 5V NPO-C0G 01005

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RADIO_WTR

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2 100PF

20% 5V NPO-C0G 01005

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2 100PF

20% 5V NPO-C0G 01005

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20% 5V NPO-C0G 01005

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2 100PF

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2 100PF

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2 100PF

20% 5V NPO-C0G 01005

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2 100PF

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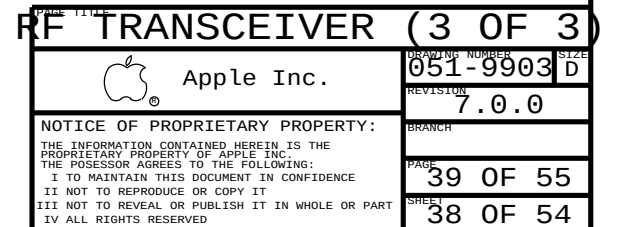
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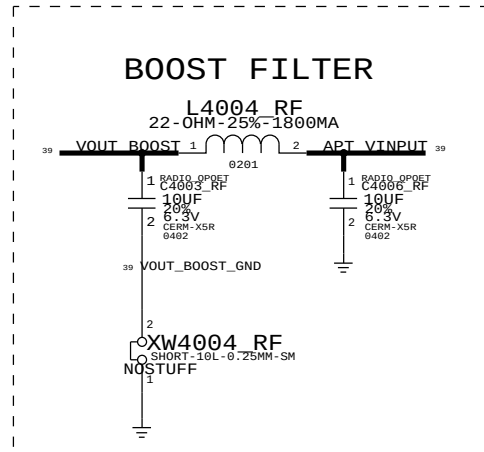
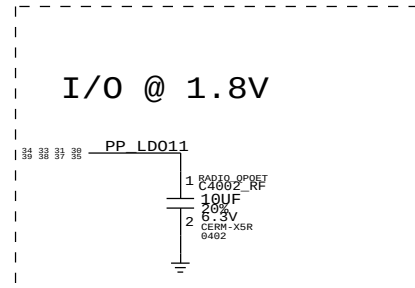
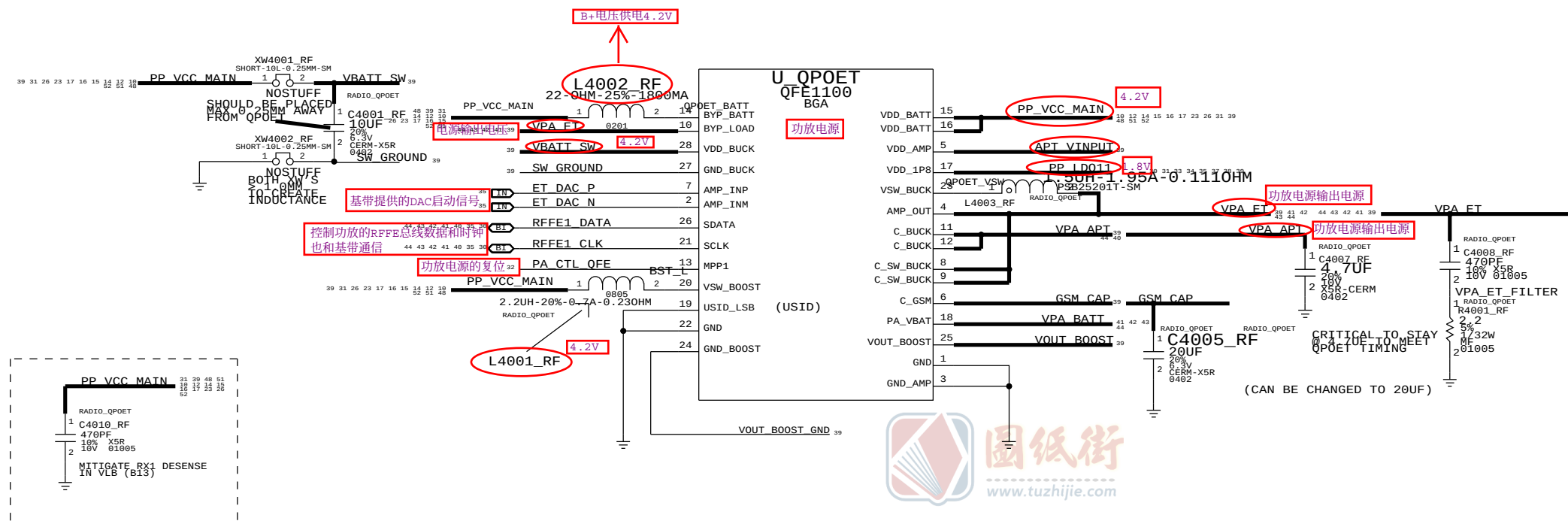
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C1019
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U1002



QFE DCDC

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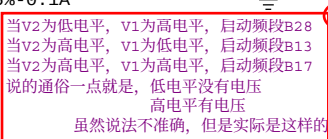
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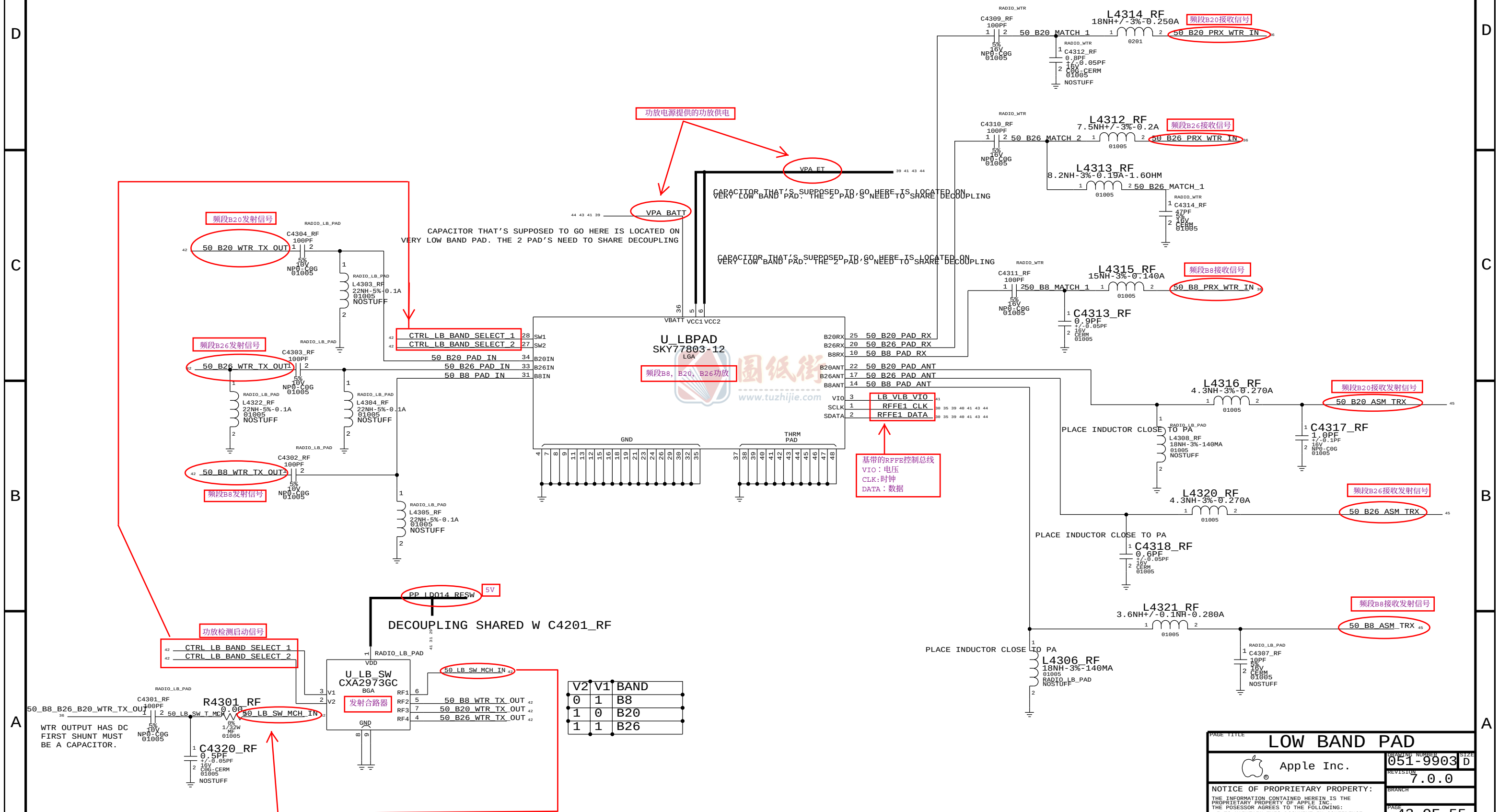
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L4322_RF
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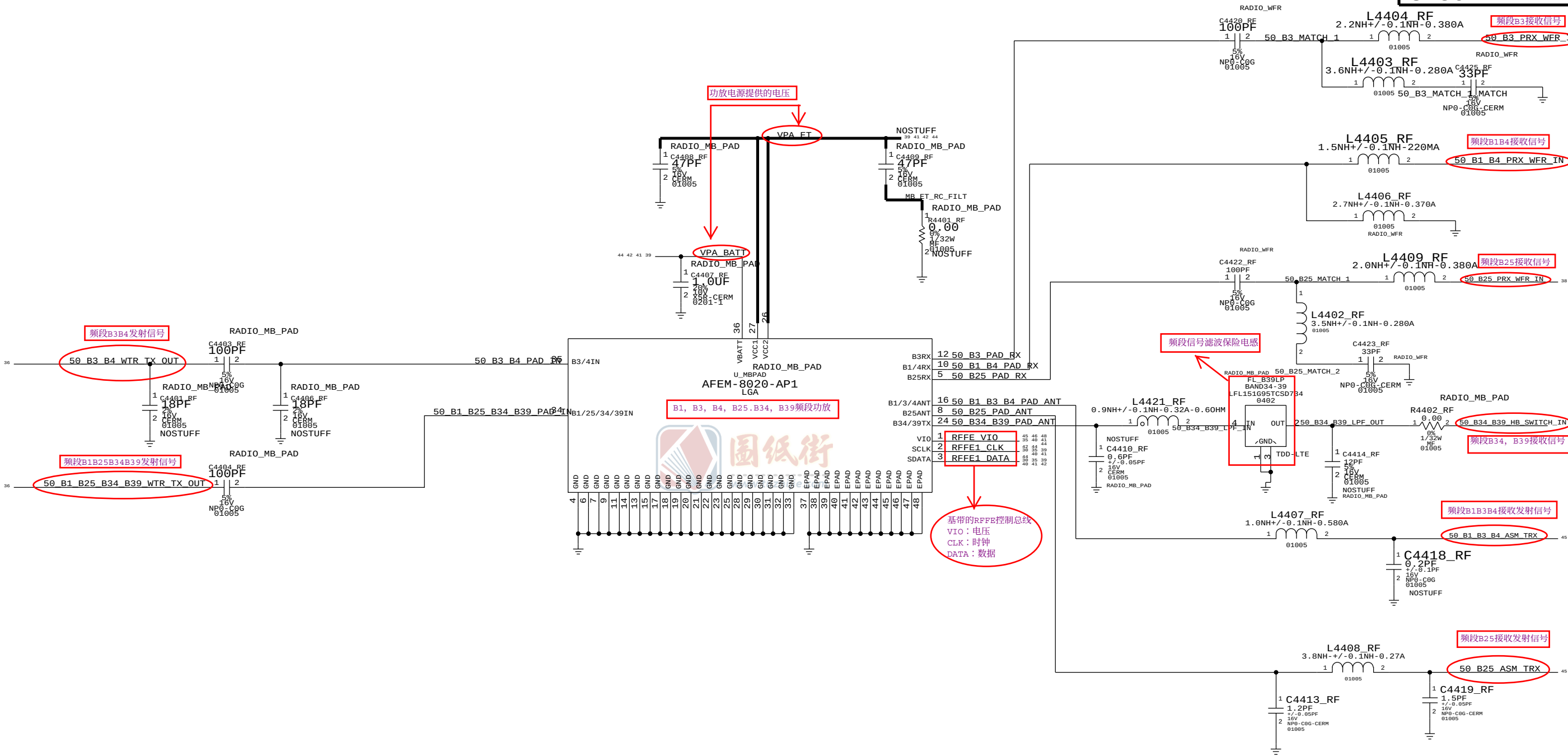
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		SHEET		42 OF 54	

MID BAND PAD (B1, B25, B3, B4, B34, B39)

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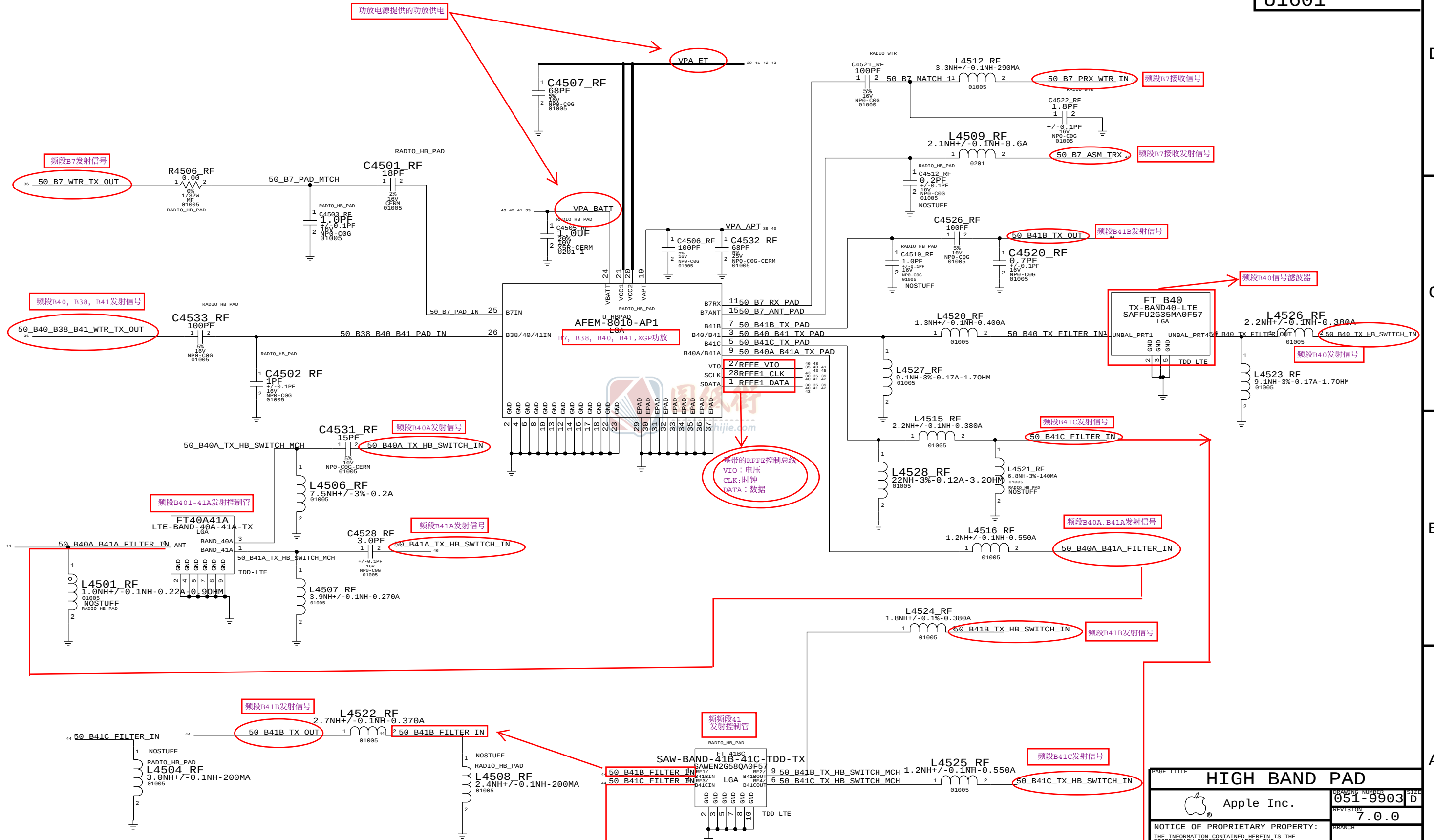


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HIGH BAND PAD (B7, B38, B40, B41, XGP)

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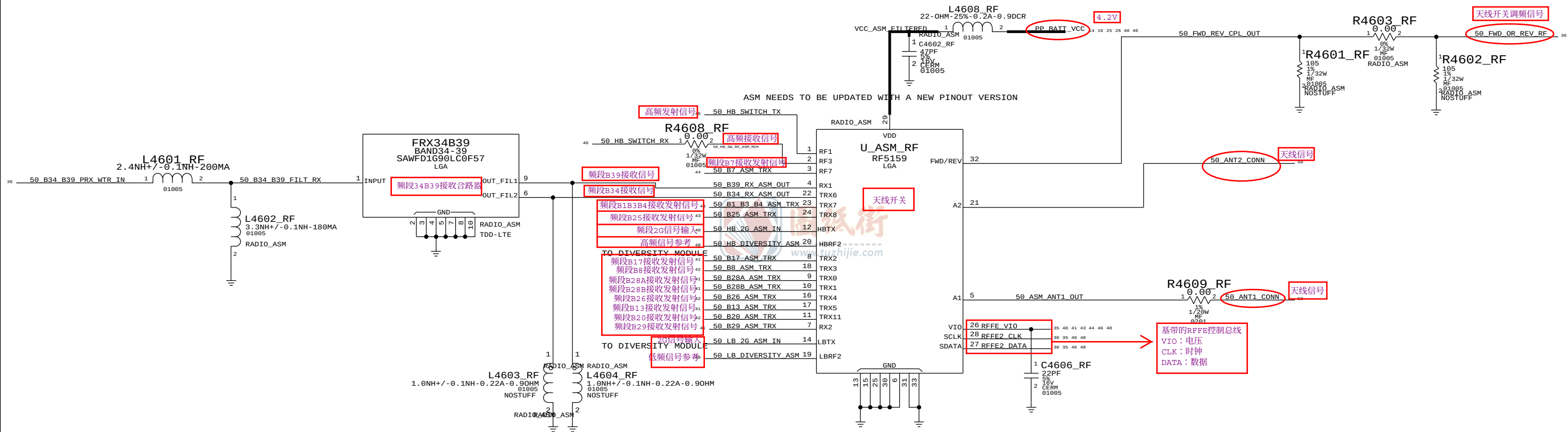
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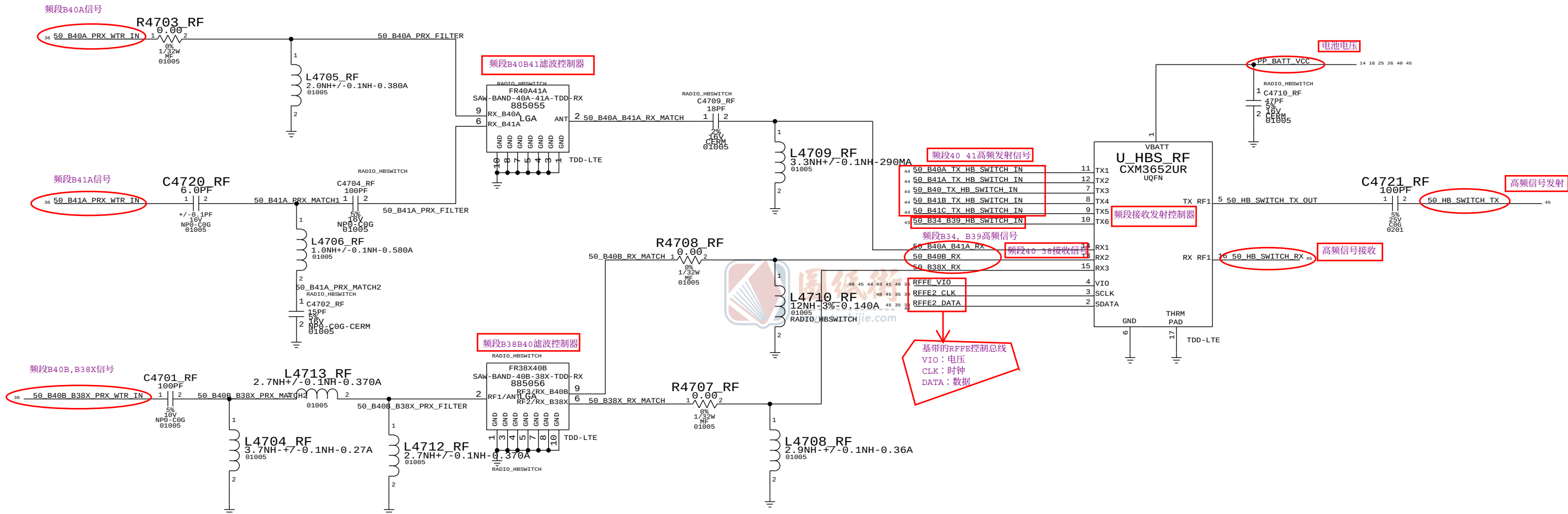
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RX DIVERSITY (1)

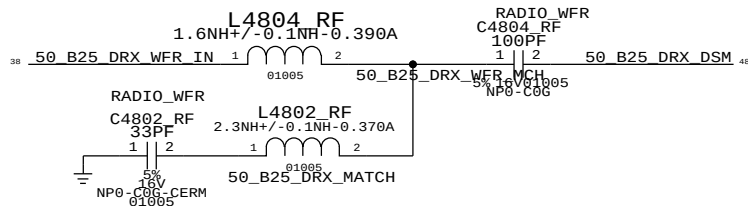
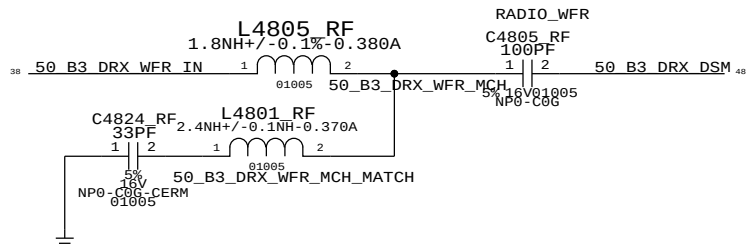
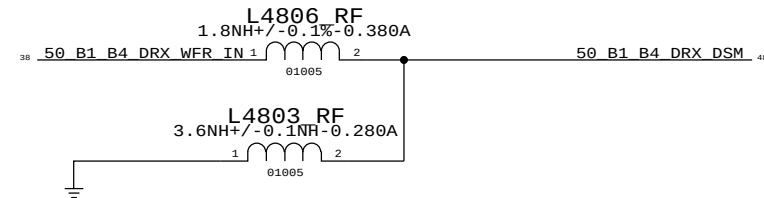
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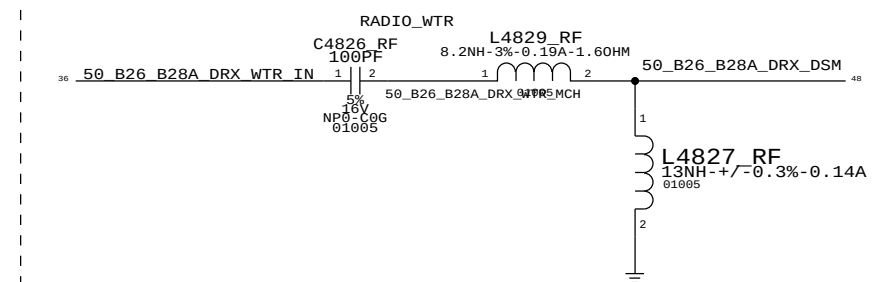
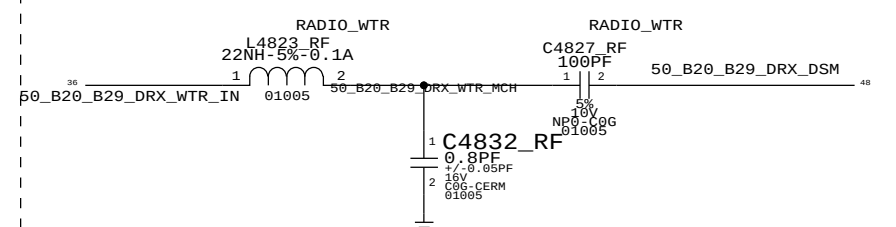
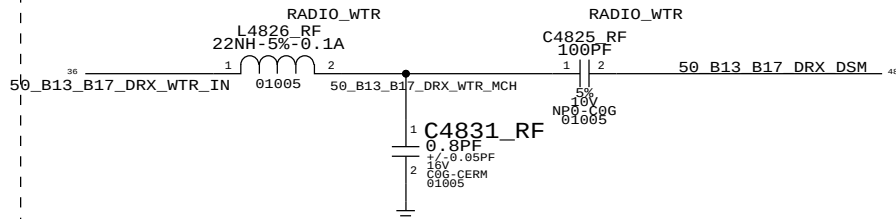
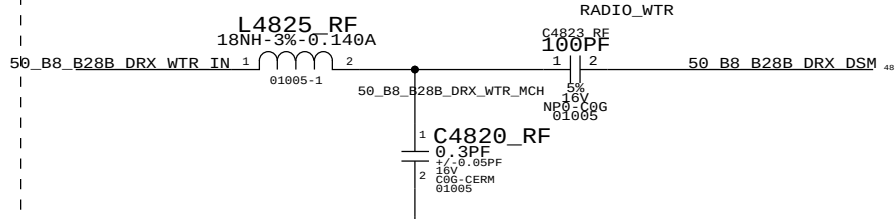
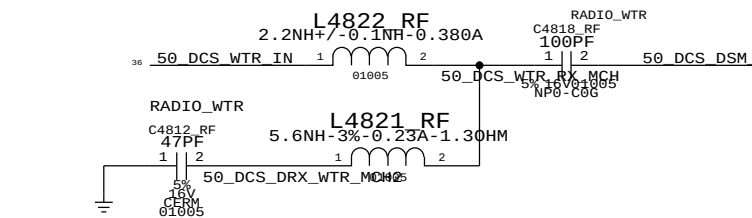
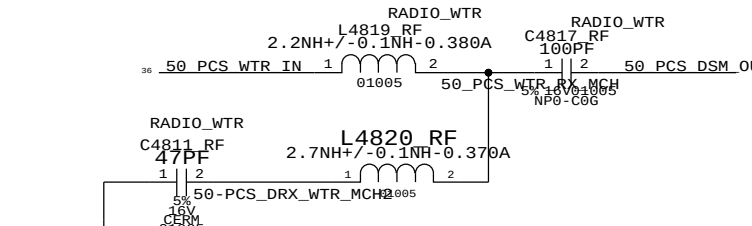
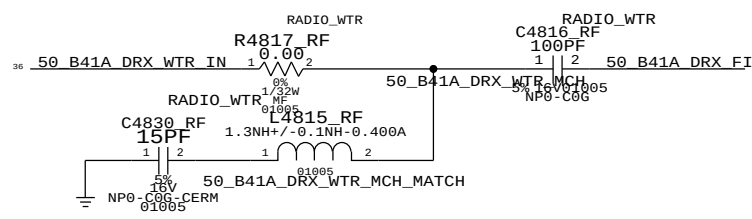
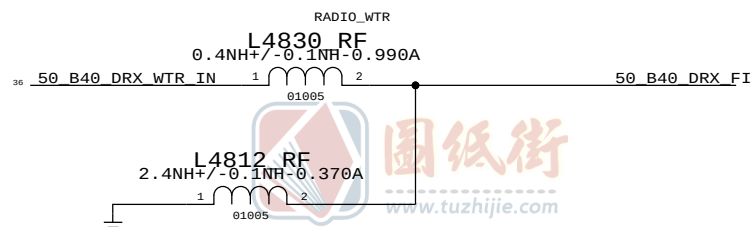
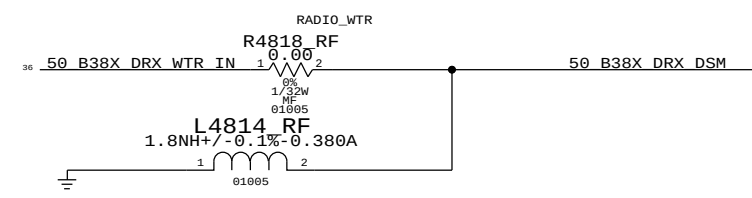
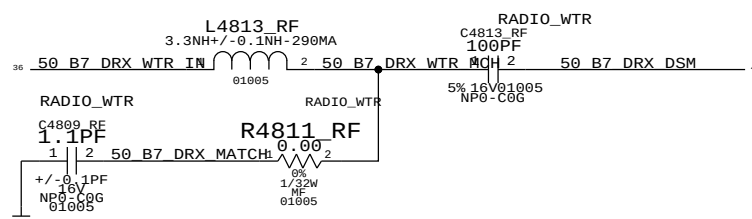
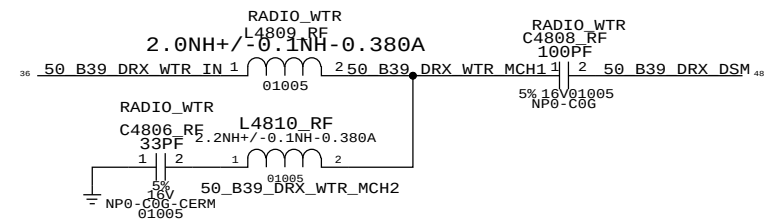
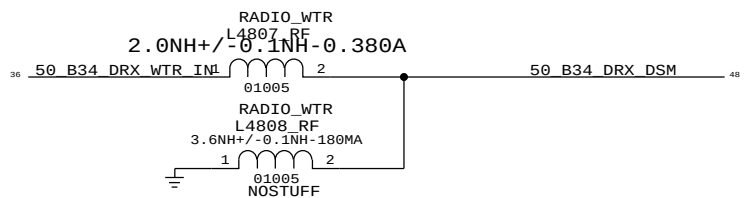
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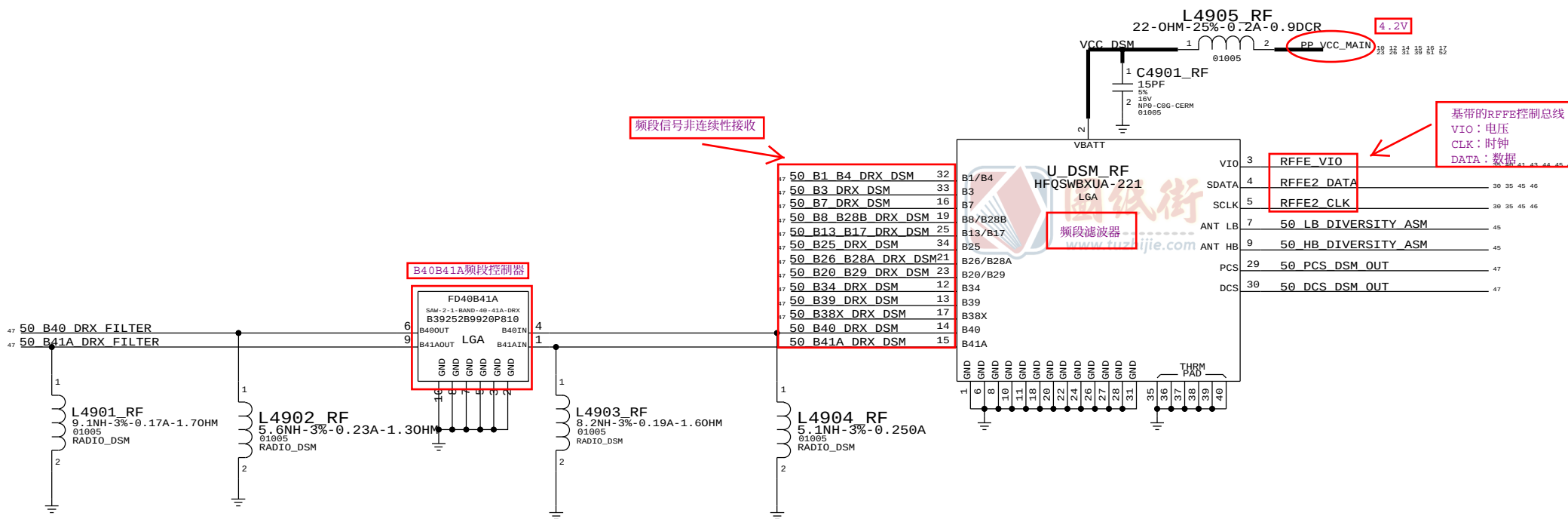


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RX DIVERSITY (2)

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L1900
U1901

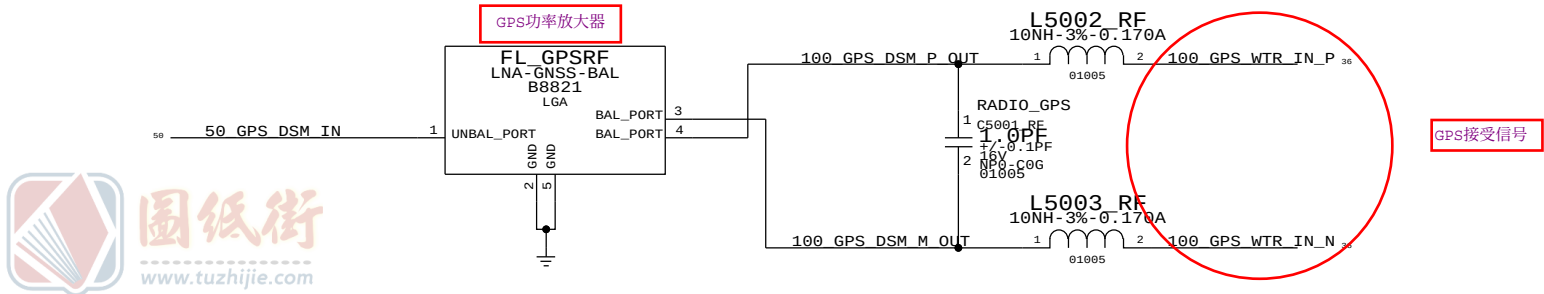


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GPS

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C1900
R1900
L1900
U1901

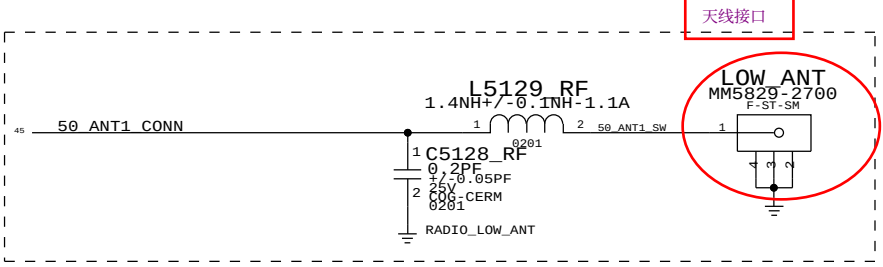
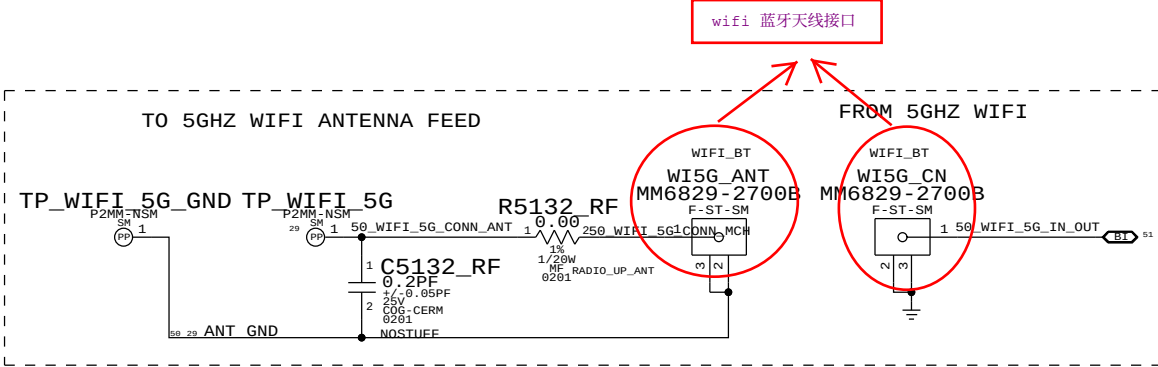
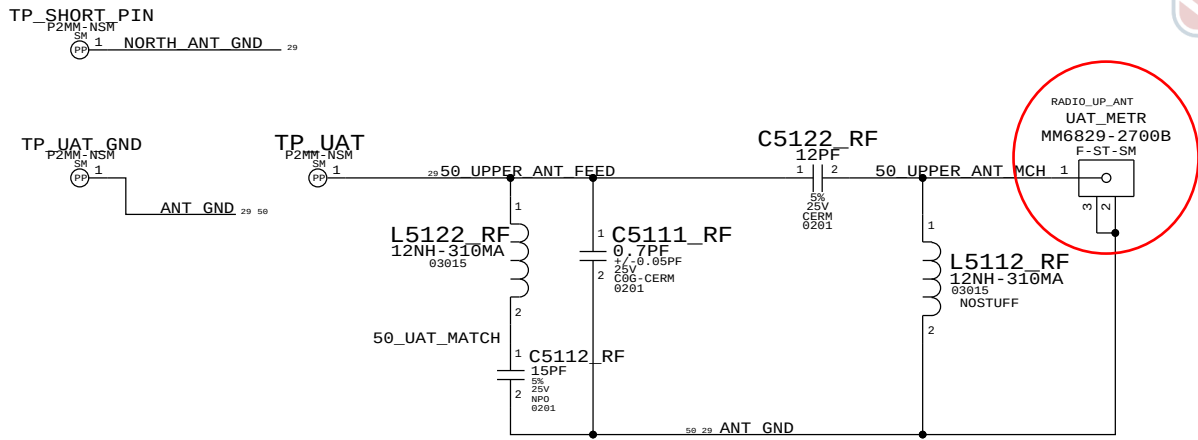
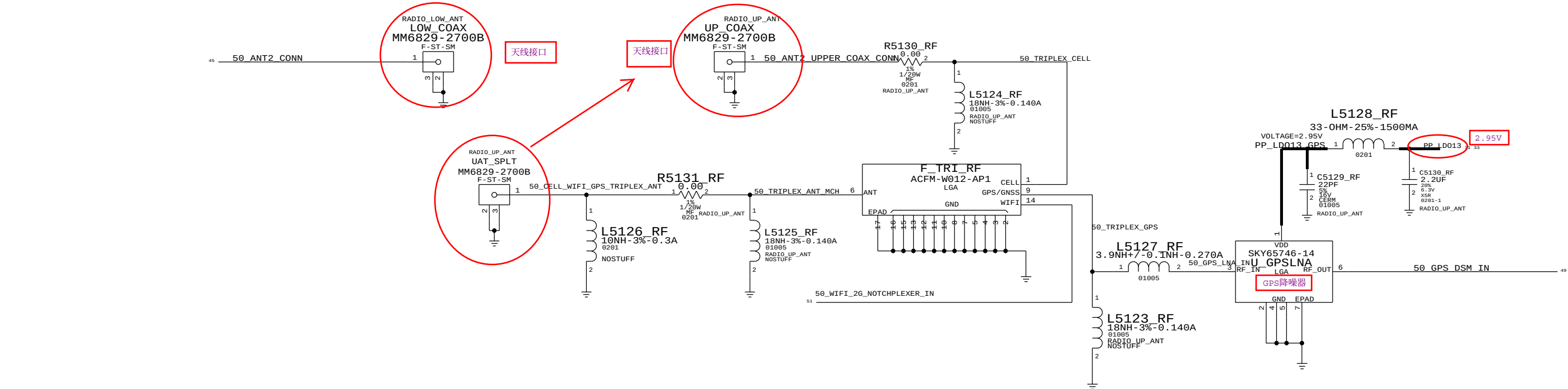


GPS	
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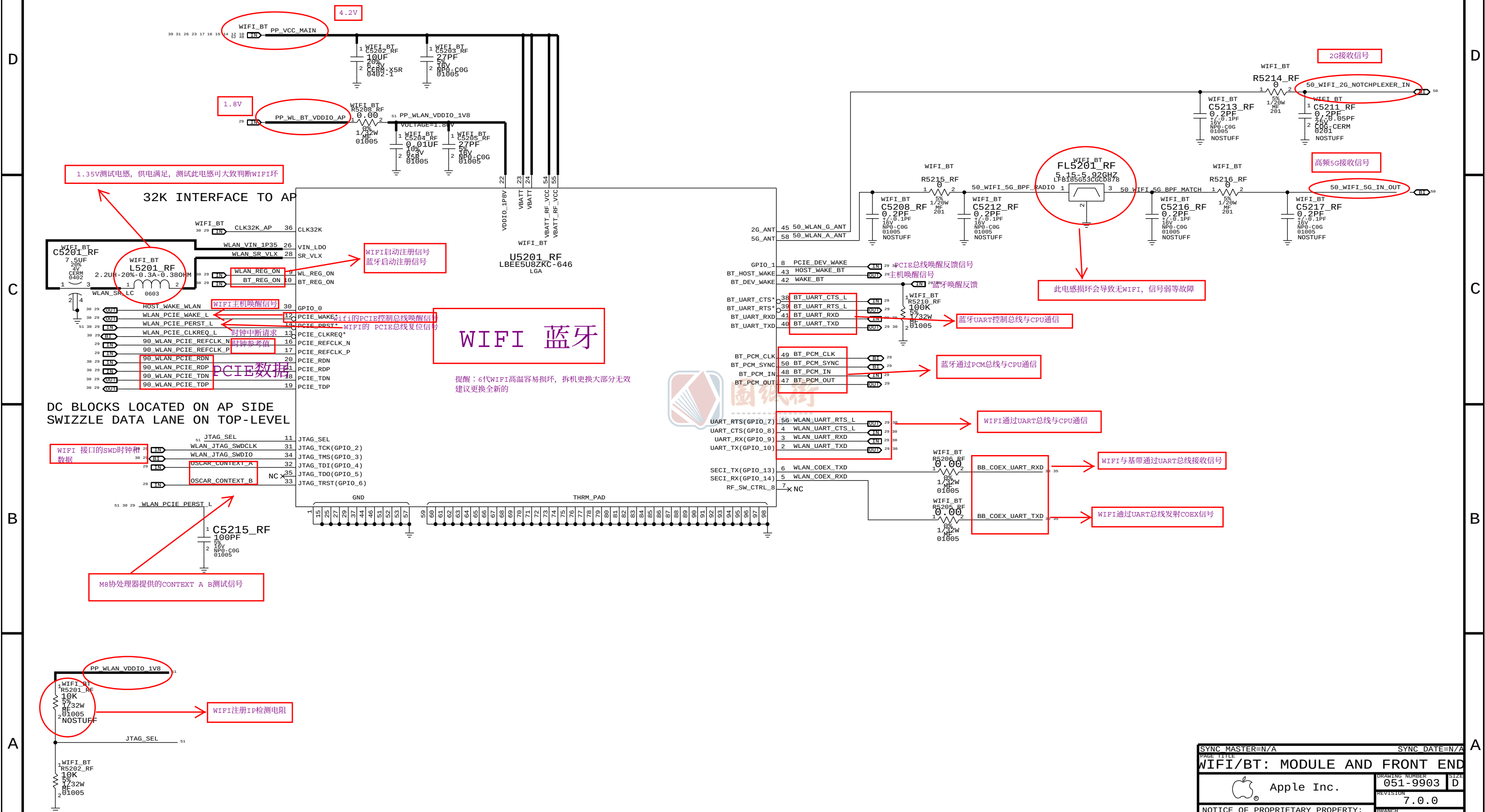
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TEST & COAX CONNECTOR FOR LOWER SECTION OF MLB



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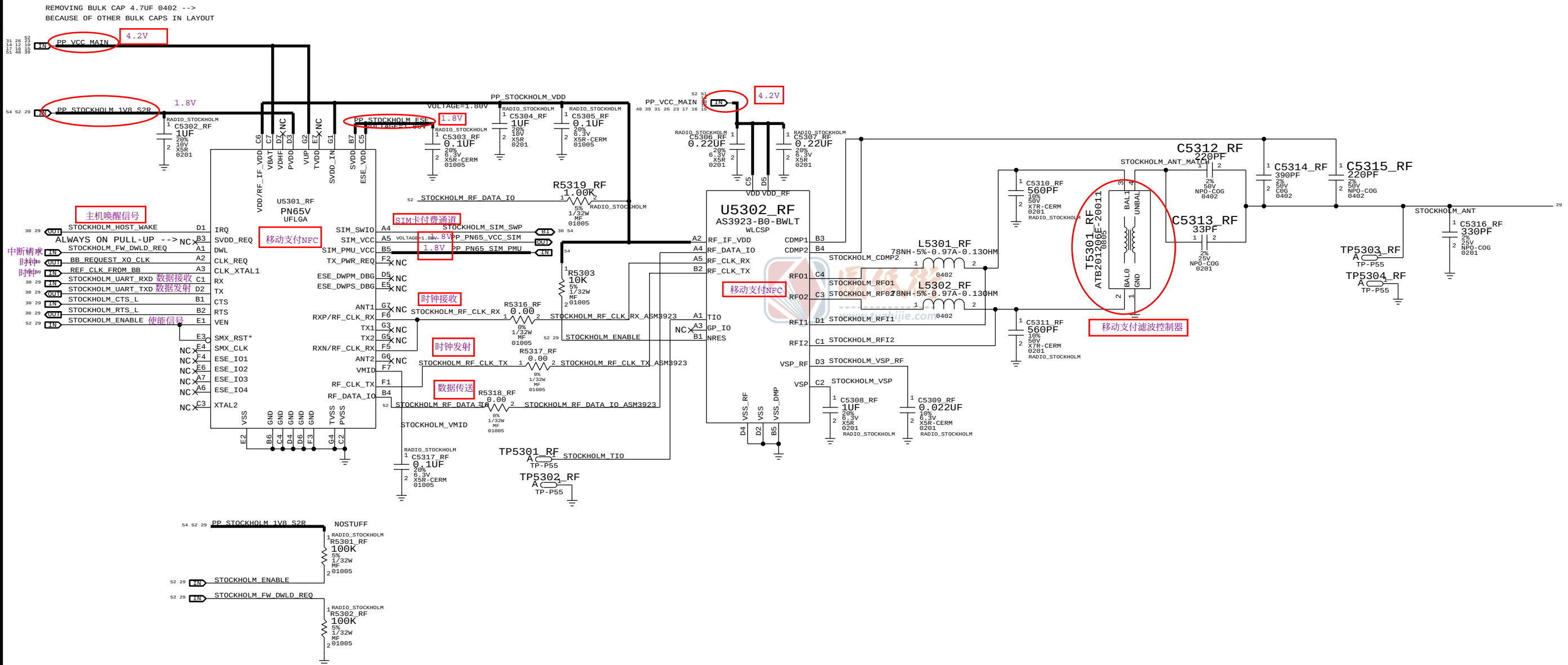
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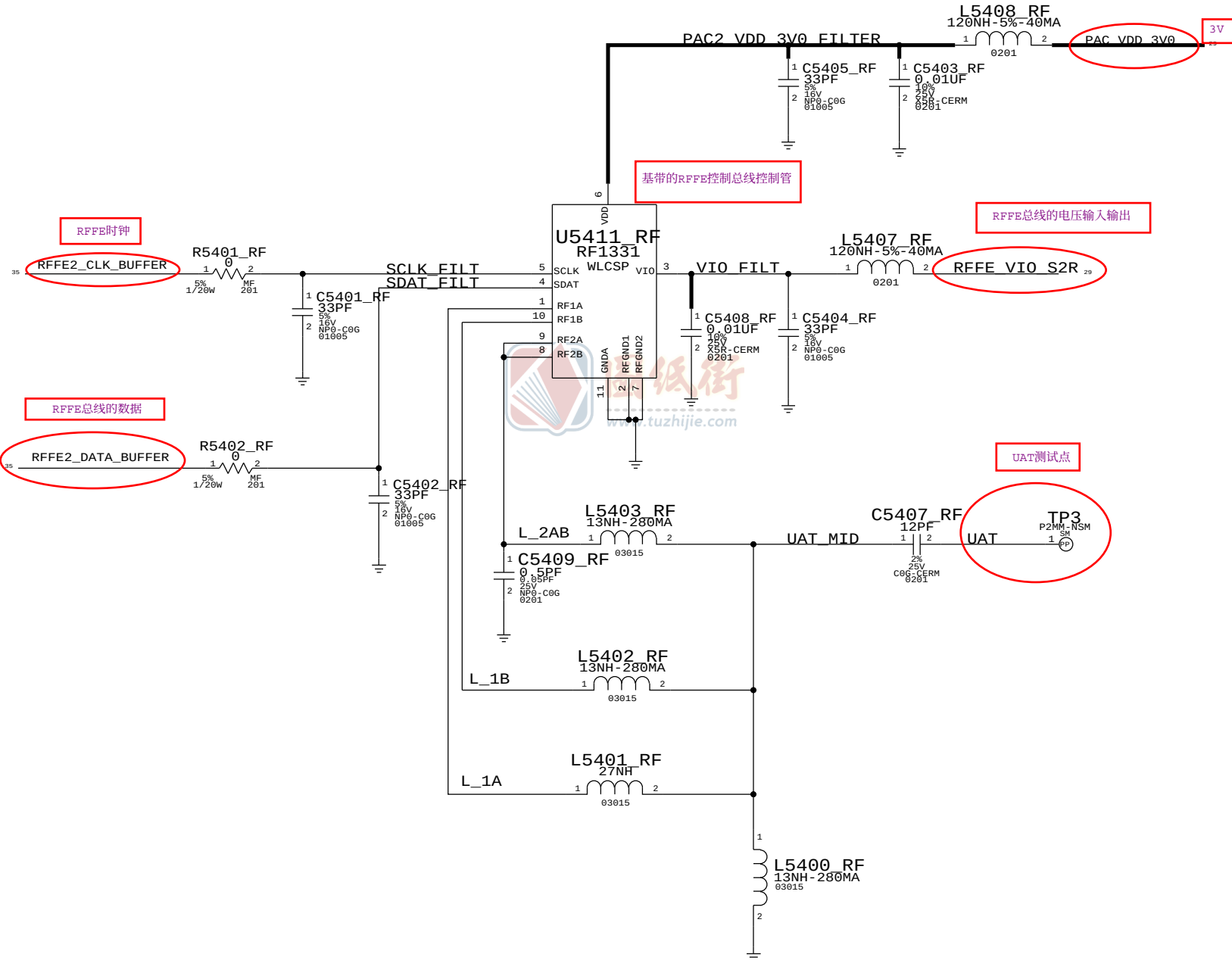
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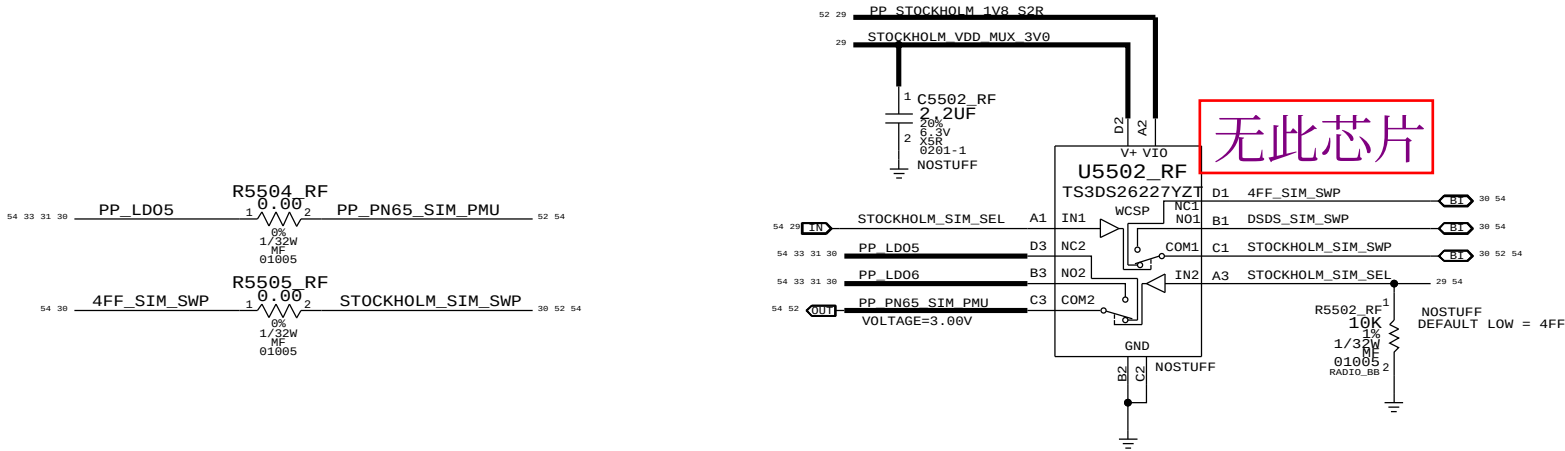
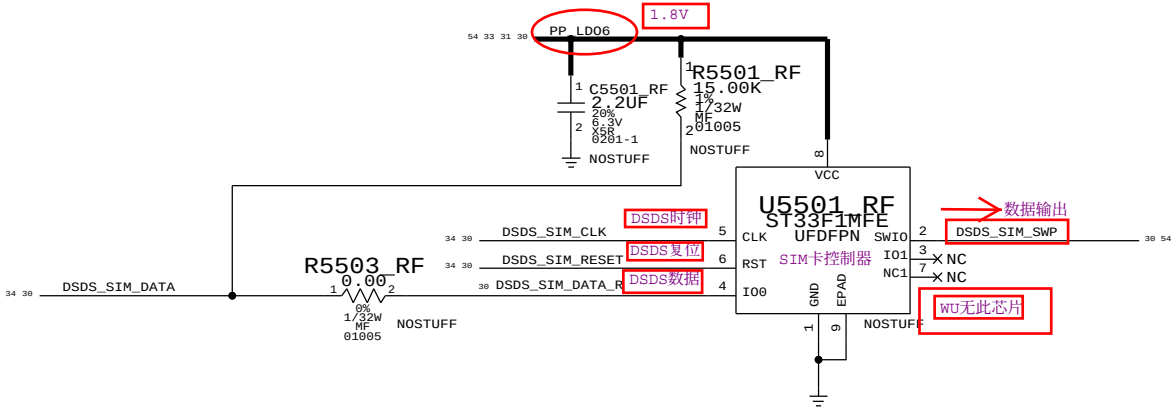
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JUMPER		
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